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October 2014

FMMT549 PNP Low-Saturation Transistor

Features

- · This device is designed with high-current gain and low-saturation voltage with collector currents up to 2 A continuous.
- · Sourced from process PB.



1. Base 2. Emitter 3. Collector

Ordering Information

Part Number	Marking	Package	Packing Method
FMMT549	549	SSOT 3L	Tape and Reel

Absolute Maximum Ratings(1),(2)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at T_A = 25°C unless otherwise noted.

Symbol	Parameter		Value	Unit	
V _{CEO}	Collector-Emitter Voltage		-30	V	
V _{CBO}	Collector-Base Voltage		-35	V	
V _{EBO}	Emitter-Base Voltage		-5	V	
I _C	Collector Current	Continuous	-1	A	
		Peak Pulse Current	-2		
TJ	Junction Temperature		150	°C	
T _{STG}	Storage Temperature Range		-55 to +150	°C	

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

Thermal Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Max.	Unit
P _D	Total Device Dissipation, by R _{θJA}	500	mW
	Derate Above 25°C	4	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	250	°C/W

Note:

3. Device is mounted on FR-4 PCB 4.5 inch X 5 inch, mounting pad 0.02 in² of 2 oz copper.

Electrical Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C = -10 mA, I _B = 0	-30		V
BV _{CBO}	Collector-Base Breakdown Voltage	$I_C = -100 \mu A, I_E = 0$	-35		V
BV _{EBO}	Emitter-Base Breakdown Voltage	I _E = -100 μA, I _C = 0	-5.0		V
Ісво	Collector Cut-Off Current	$V_{CB} = -30 \text{ V}, I_{E} = 0$		-100	nA
		$V_{CB} = -30 \text{ V}, I_{E} = 0,$ $T_{A} = 100^{\circ}\text{C}$		-10	μА
I _{EBO}	Emitter Cut-Off Current	$V_{EB} = -4.0 \text{ V}, I_{C} = 0$		-100	nA
	DC Current Gain ⁽⁴⁾	$V_{CE} = -2.0 \text{ V}, I_{C} = -50 \text{ mA}$	70		
h _{FE}		V_{CE} = -2.0 V, I_{C} = -500 mA	100	300	
		V _{CE} = -2.0 V, I _C = -1 A	80		
		V _{CE} = -2.0 V, I _C = -2 A	40		
V _{CE} (sat)	Collector-Emitter Saturation Voltage ⁽⁴⁾	$I_C = -1 \text{ A}, I_B = -100 \text{ mA}$		-500	mV
		$I_C = -2 \text{ A}, I_B = -200 \text{ mA}$		-750	1110
V _{BE} (sat)	Base-Emitter Saturation Voltage ⁽⁴⁾	$I_C = -1 \text{ A}, I_B = -100 \text{ mA}$		-1.25	V
V _{BE} (on)	Base-Emitter On Voltage ⁽⁴⁾	I _C = -1 A, V _{CE} = -2.0 V		-1.0	V
f _T	Current Gain Bandwidth Product	I _C = -100 mA, V _{CE} = -5 V, f = 100 MHz	100		MHz
C _{obo}	Output Capacitance	V _{CB} = -10 V, I _E = 0, f = 1 MHz		25	pF

Note:

4. Pulse test: pulse width \leq 300 μ s, duty cycle \leq 2.0%

Typical Performance Characteristics

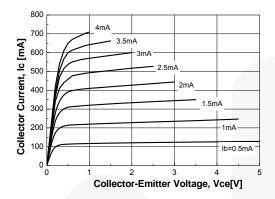


Figure 1. Collector-Emitter Voltage vs.
Collector Current

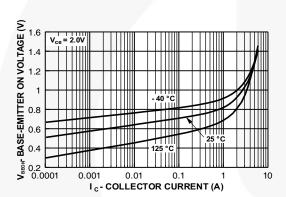


Figure 4. Base-Emitter On Voltage vs.
Collector Current

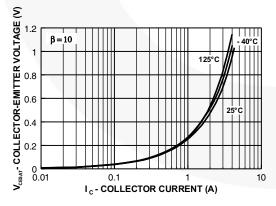


Figure 5. Collector-Emitter Saturation Voltage vs.
Collector Current

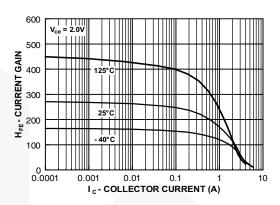


Figure 2. Current Gain vs. Collector Current

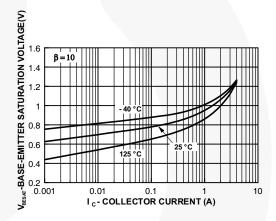


Figure 5. Base-Emitter Saturation Voltage vs. Collector Current

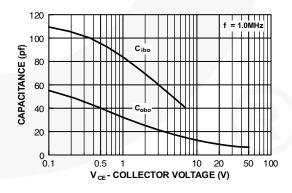


Figure 6. Input / Output Capacitance vs.
Reverse Bias Voltage

3

Physical Dimensions 0.95 2.92±0.12-A 3 В 1.40 1.40±0.12 2.20 2 (0.29)--1.00◆ 0.20M A B 0.95 -1.90 -1.90 LAND PATTERN RECOMMENDATION SEE DETAIL A--1.12 MAX 0.10 (0.94)△ 0.10M C C 2.51 ± 0.20 GAGE PLANE NOTES: UNLESS OTHERWISE SPECIFIED 0.20 NO JEDEC REFERENCE AS OF AUGUST 2003 ALL DIMENSIONS ARE IN MILLIMETERS. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR EXTRUSIONS. DIMENSIONING AND TOLERANCING PER ASME Y14.5M — 1994. 0.43 0.33 SEATING PLANE (0.56)DETAIL A SCALE: 50:1 MA03BREVB

Figure 7. MOLDED PACKAGE, SUPERSOT, 3-LEAD





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Definition of Terms

Definition of Terms			
Datasheet Identification	Product Status	Definition	
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.	
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Rev. 171

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