

# BAS21M3T5G

## 250V High Voltage Switching Diode

The BAS21M3T5G device is a spin-off of our popular SOT-23 three-leaded device. It is designed for high voltage switching applications and is housed in the SOT-723 surface mount package. This device is ideal for low-power surface mount applications where board space is at a premium.

### Features

- Reduces Board Space
- This is a Halide-Free Device
- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These are Pb-Free Devices

### MAXIMUM RATINGS (EACH DIODE)

Rating	Symbol	Value	Unit
Reverse Voltage	$V_R$	250	Vdc
Forward Current	$I_F$	200	mAdc
Peak Forward Surge Current	$I_{FM(surge)}$	625	mAdc

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1) $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	265 2.1	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	470	$^\circ\text{C}/\text{W}$
Total Device Dissipation Alumina Substrate, (Note 2) $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	640 5.1	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	195	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature	$T_J, T_{stg}$	-55 to +150	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

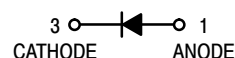
1. FR-5 =  $1.0 \times 0.75 \times 0.062$  in.
2. Alumina =  $0.4 \times 0.3 \times 0.024$  in. 99.5% alumina.



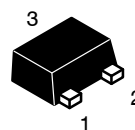
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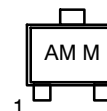
**250 V  
HIGH VOLTAGE  
SWITCHING DIODE**



### MARKING DIAGRAM



SOT-723  
CASE 631AA  
STYLE 2



AM = Specific Device Code  
M = Date Code

### ORDERING INFORMATION

Device	Package	Shipping†
BAS21M3T5G	SOT-723 (Pb-Free)	8000 / Tape & Reel
NSVBAS21M3T5G	SOT-723 (Pb-Free)	8000 / Tape & Reel

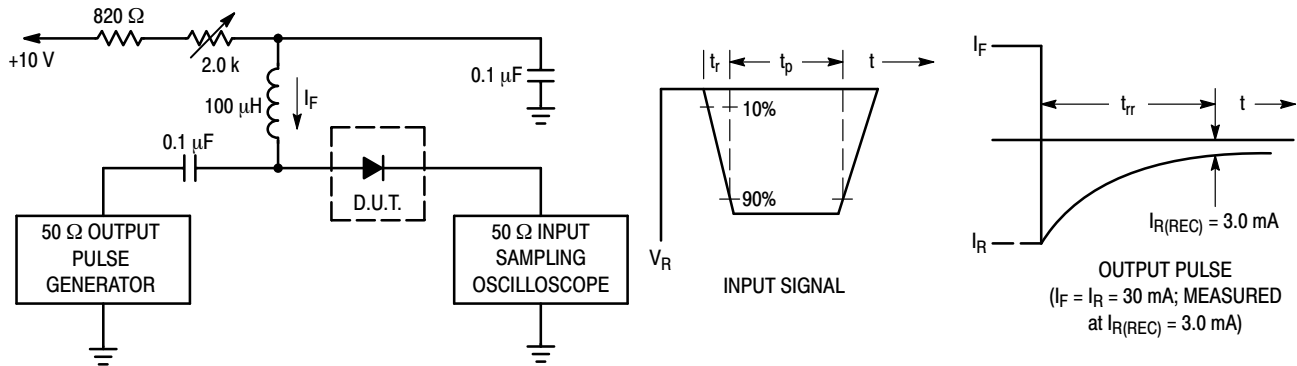
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# BAS21M3T5G

## ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
Reverse Voltage Leakage Current ( $V_R = 200\text{ Vdc}$ ) ( $V_R = 200\text{ Vdc}$ , $T_J = 150^\circ\text{C}$ )	$I_R$	-	0.1 100	$\mu\text{A}$ $\text{dc}$
Reverse Breakdown Voltage ( $I_{BR} = 100\ \mu\text{A}$ )	$V_{(BR)}$	250	-	Vdc
Forward Voltage ( $I_F = 100\ \text{mA}$ ) ( $I_F = 200\ \text{mA}$ )	$V_F$	-	1.0 1.25	Vdc
Diode Capacitance ( $V_R = 0$ , $f = 1.0\ \text{MHz}$ )	$C_D$	-	5.0	pF
Reverse Recovery Time ( $I_F = I_R = 30\ \text{mA}$ , $I_{R(REC)} = 3.0\ \text{mA}$ , $R_L = 100$ )	$t_{rr}$	-	50	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



- Notes: 1. A 2.0 k $\Omega$  variable resistor adjusted for a Forward Current ( $I_F$ ) of 30 mA.  
 2. Input pulse is adjusted so  $I_{R(\text{peak})}$  is equal to 30 mA.  
 3.  $t_p \gg t_{rr}$

**Figure 1. Recovery Time Equivalent Test Circuit**

# BAS21M3T5G

## TYPICAL CHARACTERISTICS

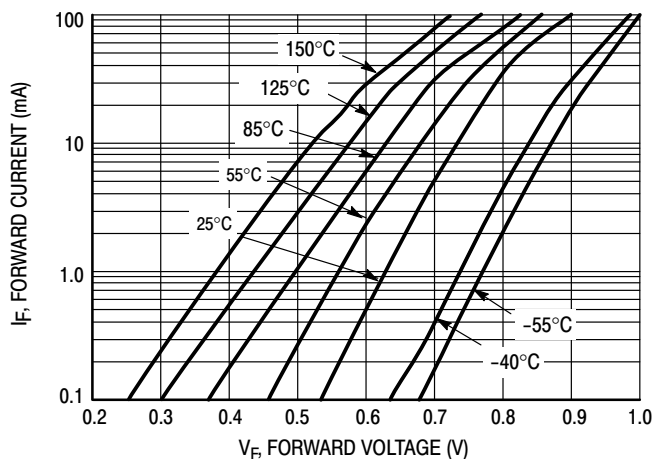


Figure 2.  $V_F$  vs.  $I_F$

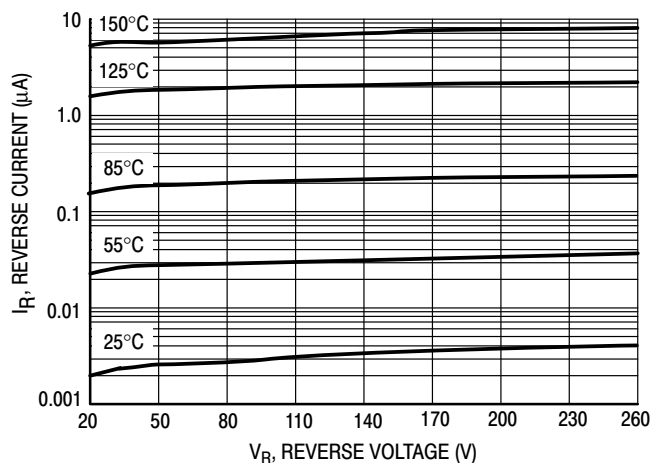


Figure 3.  $I_R$  vs.  $V_R$

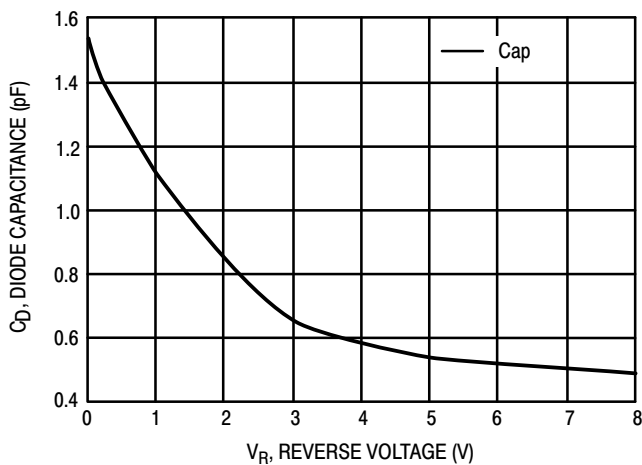


Figure 4. Capacitance

# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

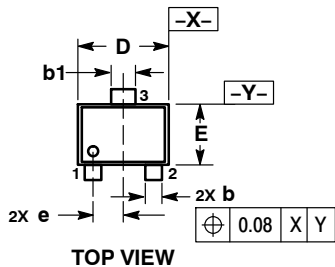
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SCALE 4:1

**SOT-723**  
CASE 631AA-01  
ISSUE D

DATE 10 AUG 2009

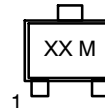


**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

MILLIMETERS			
DIM	MIN	NOM	MAX
A	0.45	0.50	0.55
b	0.15	0.21	0.27
b1	0.25	0.31	0.37
C	0.07	0.12	0.17
D	1.15	1.20	1.25
E	0.75	0.80	0.85
e	0.40 BSC		
H E	1.15	1.20	1.25
L	0.29 REF		
L2	0.15	0.20	0.25

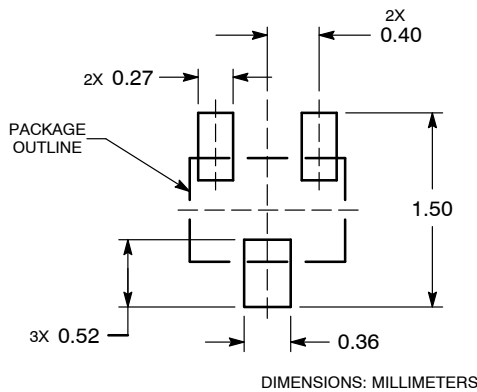
**GENERIC MARKING DIAGRAM\***



XX = Specific Device Code  
M = Date Code

- |   |  |  |  |  |
|---|--|--|--|--|
| STYLE 1:<br>PIN 1. BASE<br>2. EMITTER<br>3. COLLECTOR | STYLE 2:<br>PIN 1. ANODE<br>2. N/C<br>3. CATHODE | STYLE 3:<br>PIN 1. ANODE<br>2. ANODE<br>3. CATHODE | STYLE 4:<br>PIN 1. CATHODE<br>2. CATHODE<br>3. ANODE | STYLE 5:<br>PIN 1. GATE<br>2. SOURCE<br>3. DRAIN |
|---|--|--|--|--|

**RECOMMENDED SOLDERING FOOTPRINT\***



\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present.

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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