



UWS-Q48 Series

Open-Frame Sixteenth-Brick DOSA-Compatible,
Wide Input Isolated DC-DC Converters



Typical Units

Output Voltage (Vdc)	Output Current (A)	Input Voltage Range (Vdc)
3.3	15.0	18 to 75
5	10.0	18 to 75
12	4.5	18 to 75
24	2.0	18 to 75
48	1.0	18 to 75

FEATURES

- High efficiency, up to 91%
- 18-75 Volts DC wide input range
- Single output of 3.3, 5, 12, 24 or 48 Volts
- Up to 54 Watts total output power
- 1.30 x 0.90" x 0.36" Open-frame package
- Industry standard DOSA sixteenth-brick format and pinout
- Small footprint DC-DC converter, ideal for high current applications
- Pre-bias start-up protection
- Trimable outputs
- Operating temperature range -40 to +85°C with derating
- Stable no-load operation with no required external components
- Certified to UL 60950-1, 2nd Edition, UL 62368-1 safety approvals

SAFETY FEATURES

- Basic insulation
- 2250Vdc, Input-to-Output isolation
- Over-temperature shutdown
- Extensive self-protection shut down features
- UL 60950-1, 2nd Edition, UL 62368-1
- CAN/CSA-C22.2 NO. 60950-1
- RoHS compliant

PRODUCT OVERVIEW

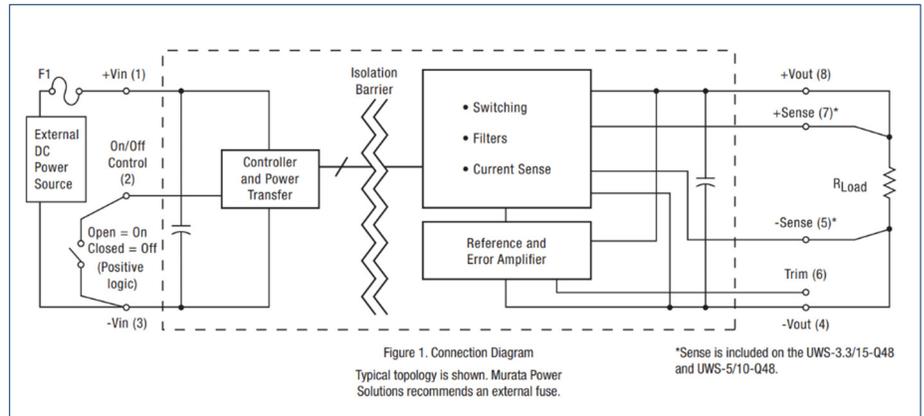
The world of "brick" DC-DC converters has seen a steady size reduction. The UWS-Q48 series makes another dramatic size shrink down to a "sixteenth brick" width (0.9 inches) while still retaining a high power output and full 2250 Volt DC I/O isolation. The converter family accepts 18 to 75 Volts DC inputs and delivers fixed regulated outputs. The UWS converters are ideal for mobile applications, datacom and telecom applications, cell phone towers, data centers, server farms and network repeaters.

The UWS outputs may be trimmed while delivering fast settling to current step loads and no adverse effects from higher capacitive loads. Excellent ripple and noise specifications assure compatibility to circuits using CPU's, ASIC's, programmable logic and FPGA's. No minimum load

is required. For systems requiring controlled startup/shutdown, the external remote On/Off control may use an open collector switch transistor.

Many self-protection features on the UWS-Q48 series avoid both converter and external circuit hazards. These include input undervoltage shutdown and overtemperature shutdown. The output of these DC-DC converters have current limit using the "hiccup" autorestart technique and the outputs may be short-circuited indefinitely. Additional features include output overvoltage and reverse conduction elimination.

The synchronous flyback topology yields high efficiency for minimal heat buildup and "no fan" operation.

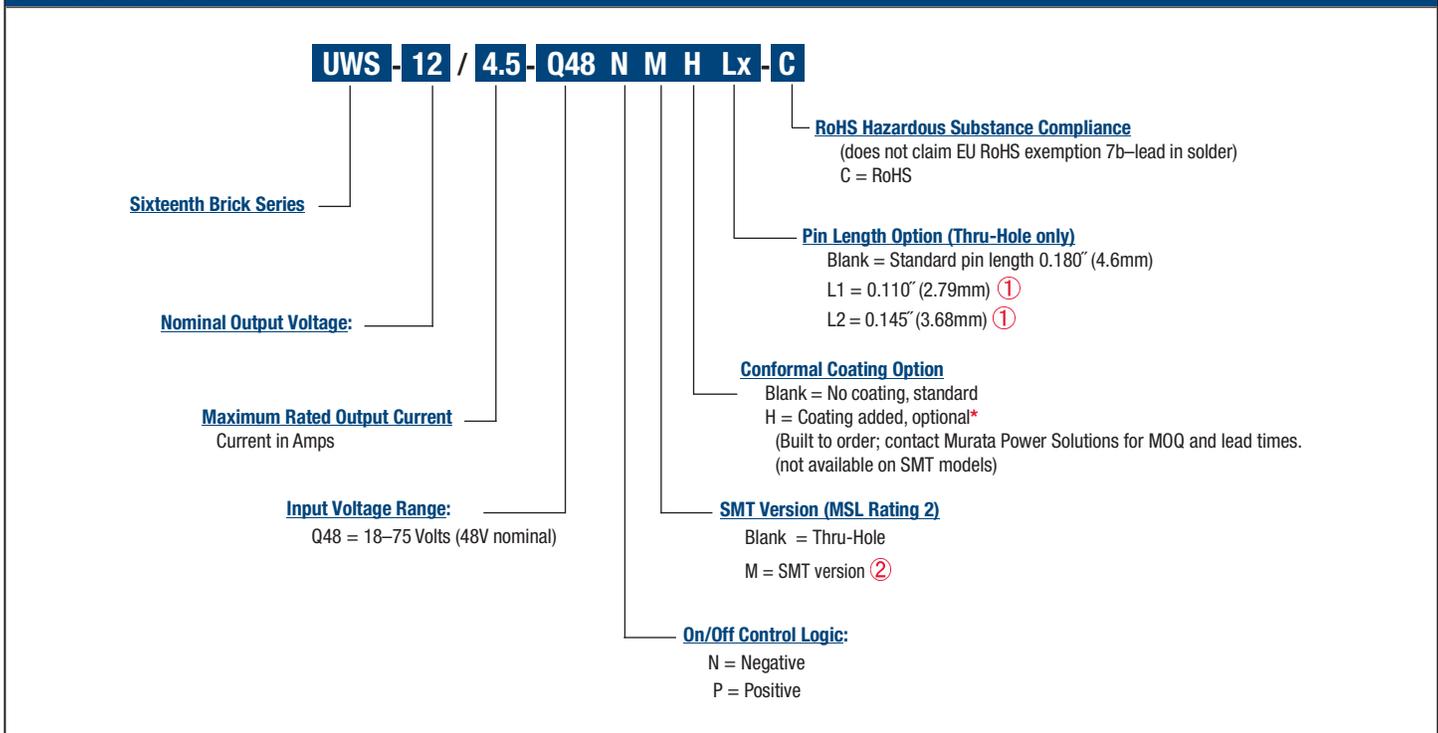


PERFORMANCE SPECIFICATIONS SUMMARY AND ORDERING GUIDE ①

Root Model ①	Output						Input				Efficiency		Package (TH)	
	V _{OUT} (V)	I _{OUT} (A, max.)	Power (W)	R/N (mV pk-pk)		Regulation (max.) ③		V _{IN} Nom. (V)	Range (V)	I _{IN} , no load (mA)	I _{IN} , full load (A)	Min.	Typ.	Case (inches)
				Typ.	Max.	Line	Load							
UWS-3.3/15-Q48	3.3	15 ④	49.5	90	125	±0.150%	±0.300%	48	18-75	25	1.16	87.5%	89.0%	1.30 x 0.90 x 0.36
UWS-5/10-Q48	5	10 ⑤	50	90	130	±0.125%	±0.125%	48	18-75	30	1.14	88.0%	91.0%	1.30 x 0.90 x 0.36
UWS-12/4.5-Q48	12	4.5 ⑥	54	115	150	±0.125%	±0.125%	48	18-75	25	1.24	89.0%	91.0%	1.30 x 0.90 x 0.36
New UWS-24/2-Q48	24	2.0	48	200	300	±0.125%	±0.125%	48	18-75	25	1.10	89.0%	90.5%	1.30 x 0.90 x 0.36
New UWS-48/1-Q48	48	1.0	48	300	400	±0.125%	±0.125%	48	18-75	25	1.10	89.0%	91.0%	1.30 x 0.90 x 0.36

- ① Please refer to the Part Number Structure when ordering.
- ② All specifications are at nominal line voltage and full load, +25°C unless otherwise noted. See detailed specifications. Output capacitors are 1 µF ceramic multilayer in parallel with 10 µF. I/O caps are necessary for our test equipment and may not be needed for your application.
- ③ Regulation specifications describe output voltage deviations from a nominal/midpoint value to either extreme (50% load step).
- ④ I_{OUT} = 13A max. if V_{IN} < 36V.
- ⑤ I_{OUT} = 8A max. if V_{IN} < 36V.
- ⑥ I_{OUT} = 3.5A max. if V_{IN} < 36V.

PART NUMBER STRUCTURE



- ① Special quantity order is required; samples available with standard pin length only.
- ② SMT (M) versions not available in sample quantities.
- ③ Some model number combinations may not be available. See website or contact your local Murata Sales Representative.

FUNCTIONAL SPECIFICATIONS, UWS-3.3/15-Q48

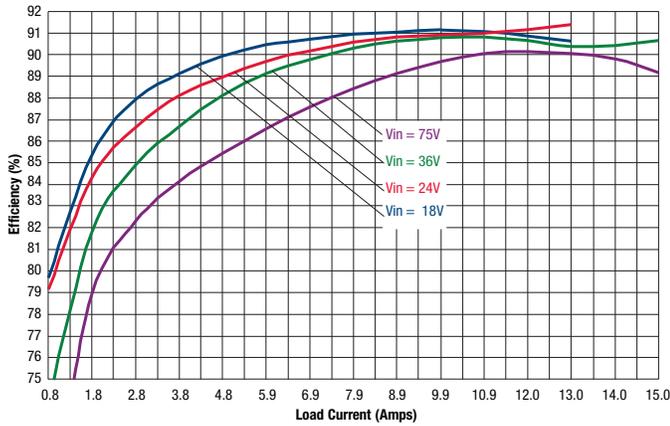
ABSOLUTE MAXIMUM RATINGS	Conditions ①	Minimum	Typical/Nominal	Maximum	Units
Input Voltage, Continuous	Full temperature range	0		80	Vdc
Input Voltage, Transient	Operating or non-operating, 100 mS max. duration	0		100	Vdc
Isolation Voltage	Input to output tested			2250	Vdc
Input Reverse Polarity	None, install external fuse		None		Vdc
On/Off Remote Control	Power on or off, referred to -Vin	0		15	Vdc
Output Power		0		50	W
Output Current	Current-limited, no damage, short-circuit protected	0		15	A
Storage Temperature Range	Vin = Zero (no power)	-55		125	°C
Absolute maximums are stress ratings. Exposure of devices to greater than any of these conditions may adversely affect long-term reliability. Proper operation under conditions other than those listed in the Performance/Functional Specifications Table is not implied or recommended.					
INPUT					
Operating voltage range		18	48	75	Vdc
Recommended External Fuse	Fast blow		6		A
Start-up threshold	Rising input voltage	16.5	17.0	17.9	Vdc
Undervoltage lockout	Falling input voltage	15.0	16.25	17.5	Vdc
Overvoltage shutdown	Rising input voltage		None		Vdc
Reverse Polarity Protection	None, install external fuse		None		Vdc
Internal Filter Type			LC		
Input current					
Full Load Conditions	Vin = nominal		1.16	1.19	A
Low Line	Vin = minimum, 13A load		2.63	2.72	A
Inrush Transient			0.4		A2-Sec.
Output in Short Circuit			100	200	mA
No Load Input current	Iout = minimum, unit=ON		25	60	mA
Shut-Down mode Input Current (Off, UV, OT)			5	10	mA
Reflected (back) ripple current ②	Measured at input with specified filter		15	30	mA, pk-pk
Pre-biased startup	External output voltage < Vset		Monotonic		
GENERAL and SAFETY					
Efficiency	Vin=48V, full load	87.5	89.0		%
	Vin=24V, full load	88.5	90.5		%
Isolation					
Isolation Voltage, Input to Output		2250			Vdc
Insulation Safety Rating			Basic		
Isolation Resistance			100		MΩ
Isolation Capacitance			1300		pF
Safety	Certified to UL-60950-1, IEC60950-1, 2nd Edition		Yes		
Calculated MTBF	Per Telcordia SR332, issue 1, class 3, ground fixed, Tambient=+25°C		3.0		Hours x 10 ⁶
DYNAMIC CHARACTERISTICS					
Fixed Switching Frequency		250	280	310	kHz
Power Up Startup Time	Power On to Vout regulated			30	mS
On/Off Startup Time	Remote ON to Vout regulated			30	mS
Dynamic Load Response	50-75-50% load step, settling time to within ±1% of Vout		100	200	μSec
Dynamic Load Peak Deviation	Same as above,		±180	±240	mV
FEATURES and OPTIONS					
Remote On/Off Control ⑥					
"N" suffix					
Negative Logic, ON state	ON=Pin grounded or external voltage	-0.1		0.8	Vdc
Negative Logic, OFF state	OFF=Pin open or external voltage	2.5		15	Vdc
Control Current	Open collector/drain, sourcing		1	2	mA
"P" suffix					
Positive Logic, ON state	ON=Pin open or external voltage	10		15	Vdc
Positive Logic, OFF state	OFF=Ground pin or external voltage	0		0.7	Vdc
Control Current	Open collector/drain		1	2	mA

FUNCTIONAL SPECIFICATIONS, UWS-3.3/15-Q48 (CONT.)

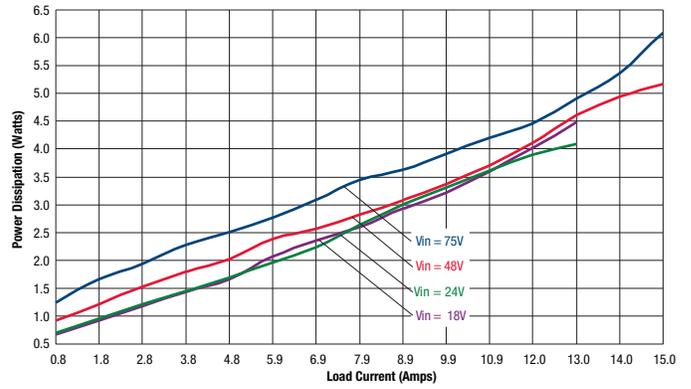
OUTPUT	Conditions ①	Minimum	Typical/Nominal	Maximum	Units
Total Output Power	See Derating	0.0	49.5	50	W
Voltage					
Nominal Output Voltage	No trim	3.267	3.30	3.333	Vdc
Setting Accuracy	At 50% load		±1		% of Vnom.
Output Voltage Range	User-adjustable	-10		10	% of Vnom.
Overvoltage Protection	Via magnetic feedback	4.0	4.3	4.9	Vdc
Current					
Output Current Range	Vin=18V-36V	0.0		13.0	A
Output Current Range	Vin=36V-75V	0.0		15.0	A
Minimum Load			No minimum load		
Current Limit Inception	98% of Vnom., after warmup	18.4	21.9	25.4	A
Short Circuit					
Short Circuit Current	Hiccup technique, autorecovery within ±1.25% of Vout		0.6		A
Short Circuit Duration (remove short for recovery)	Output shorted to ground, no damage		Continuous		
Short circuit protection method	Current limiting				
Regulation ⑦					
Line Regulation	Vin=min. to max., Vout=nom., full load			±0.15	%
Load Regulation	Iout=min. to max., Vin=48V			±0.30	%
Ripple and Noise	With a 1uF 10uF output caps		90	125	mV pk-pk
	With a 1uF 100uF output caps		60		mV pk-pk
Temperature Coefficient	At all outputs		±0.02		% of Vnom./°C
Remote Sense Compensation ⑧	Sense connected at load			10	% of Vout
Maximum Capacitive Load	Constant resistance mode, low ESR	0	10,000		μF
MECHANICAL					
Outline Dimensions			1.30x0.90x0.36		Inches
(Please refer to outline drawing)	LxWxH		33.0x22.9x9.1		mm
Weight			0.48		Ounces
			13.6		Grams
Through Hole Pin Diameter			0.040 & 0.060		Inches
			1.02 & 1.52		mm
Through Hole Pin Material			Copper alloy		
TH Pin Plating Metal and Thickness	Nickel subplate	50			μ-inches
	Gold overplate	5			μ-inches
EMI/RFI Shielding			None		
ENVIRONMENTAL					
Operating Ambient Temperature Range	See derating, full power, natural convection	-40		85	°C
Operating Case Temperature Range	No derating, full power, natural convection	-40		105	°C
Storage Temperature	Vin = Zero (no power)	-55		125	°C
Thermal Protection/Shutdown	Measured in center	115	125	130	°C
Electromagnetic Interference Conducted, EN55022/CISPR22	External filter is required		A		Class

TYPICAL PERFORMANCE DATA, UWS-3.3/15-Q48

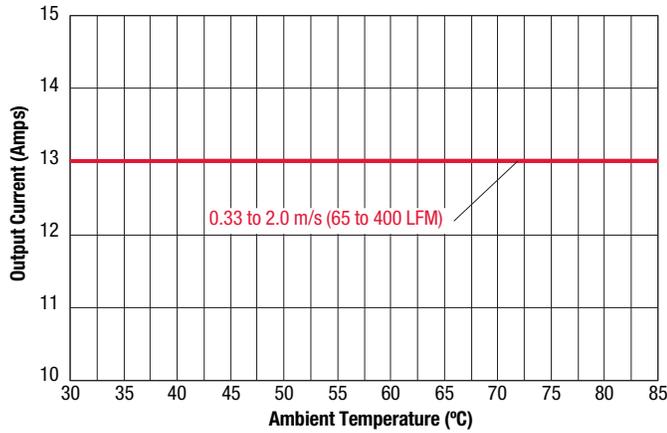
Efficiency vs. Line Voltage and Load Current @ 25°C



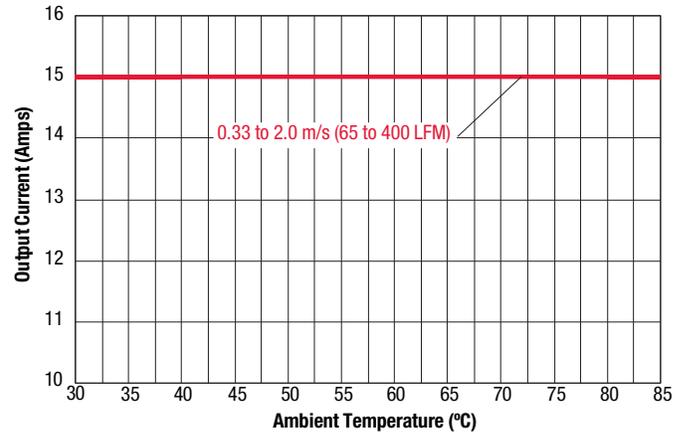
Power Dissipation vs. Load Current @ 25°C



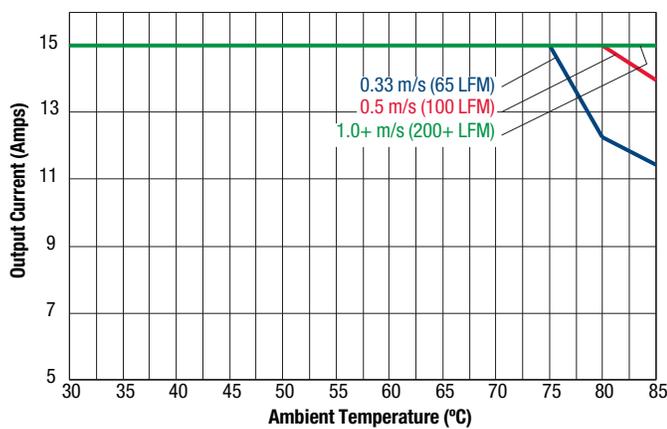
Maximum Current Temperature Derating at Sea Level
($V_{IN} = 18$ and $V_{IN} = 24$, airflow is from pin 3 to pin 1)



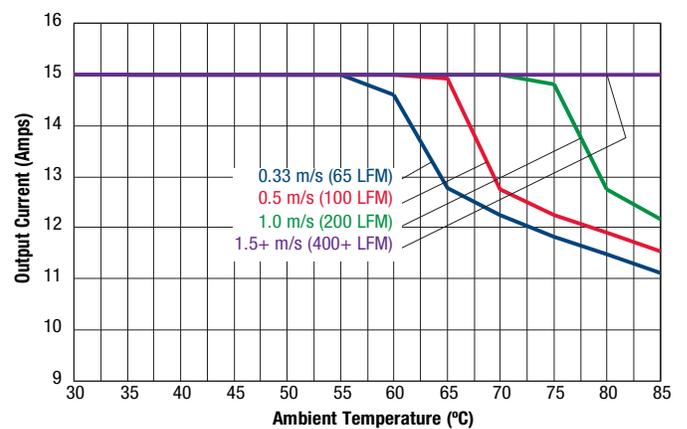
Maximum Current Temperature Derating at Sea Level
($V_{IN} = 36$ and $V_{IN} = 48$, airflow is from pin 3 to pin 1)



Maximum Current Temperature Derating at Sea Level
($V_{IN} = 60$ V, airflow is from pin 3 to pin 1)

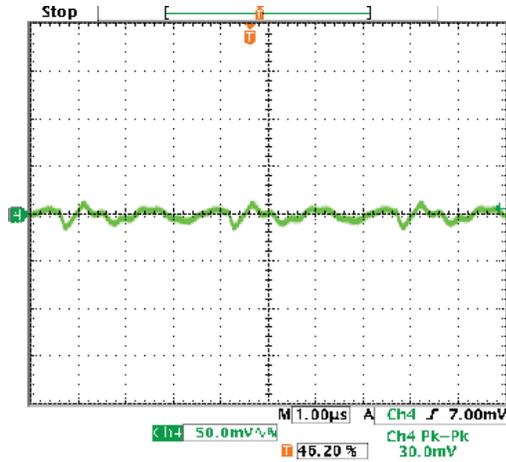


Maximum Current Temperature Derating at Sea Level
($V_{IN} = 75$ V, airflow is from pin 3 to pin 1)

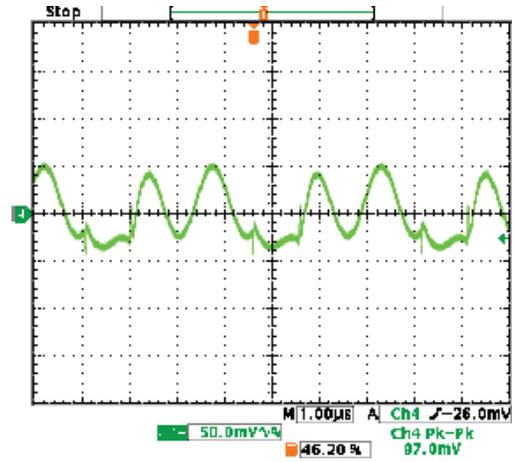


TYPICAL PERFORMANCE DATA, UWS-3.3/15-Q48

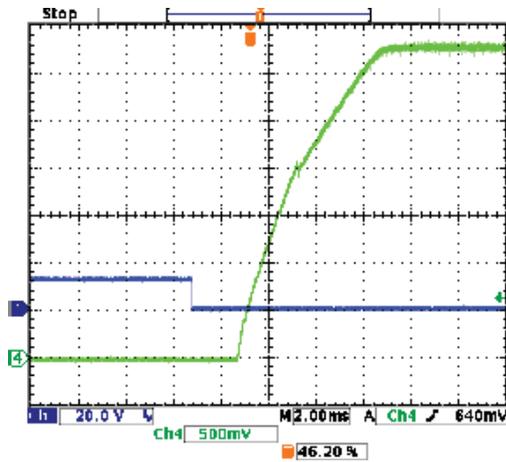
Output Ripple and Noise (Vin=48V, Vout=nom., Iout=no load, Cload=1μF ceramic || 10μF tantalum, Ta=+25°C., ScopeBW=20MHz)



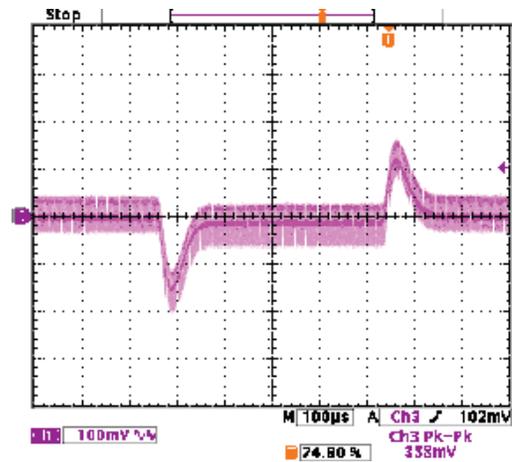
Output Ripple and Noise (Vin=48V, Vout=nom., Iout=15A, Cload=1μF ceramic || 10μF tantalum, Ta=+25°C., ScopeBW=20MHz)



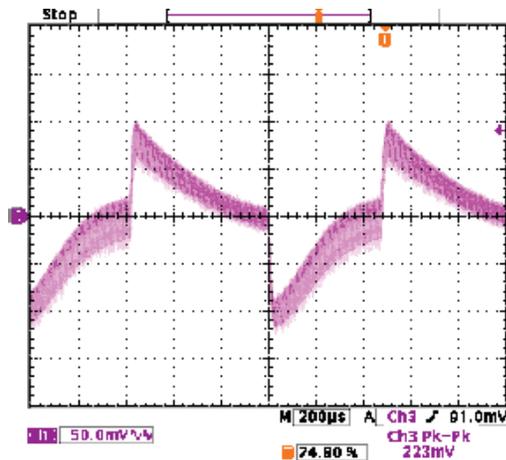
On/Off Enable Delay (Vin=48V, Vout=nom., Iout=15A, Cload=0 μF, Ta=+25°C., ScopeBW=20MHz) Trace 1=Enable, Trace 4=Vout



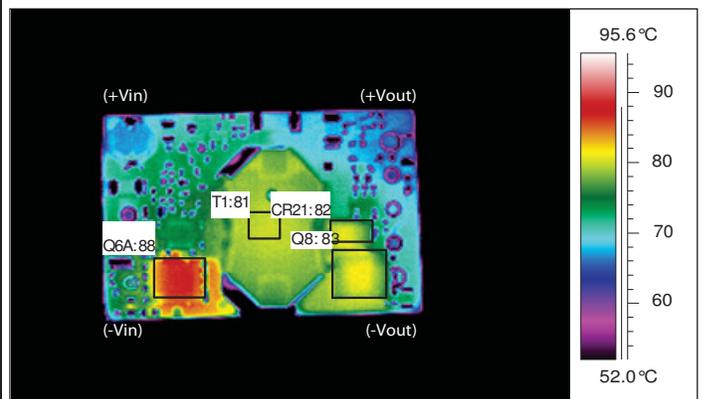
Step Load Transient Response (Vin=48V, Vout=nom., Iout=50-75-50% of full load, Cload=1μF ceramic || 10μF tantalum, Ta=+25°C., ScopeBW=20MHz)



Step Load Transient Response (Vin=48V, Vout=nom., Iout=50-75-50% of full load, Cload=10,000 μF, Ta=+25°C., ScopeBW=20MHz)



Thermal image with hot spot at full load current with 25 °C ambient temperature. Natural convection is used with no forced airflow. Identifiable and recommended maximum value to be verified in application. Vin=48V, Q6 max Temp=120 °C/IPC9592 guidelines.



FUNCTIONAL SPECIFICATIONS, UWS-5/10-Q48

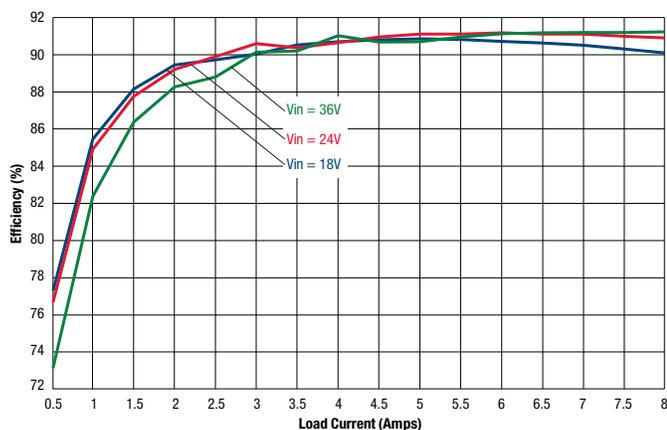
ABSOLUTE MAXIMUM RATINGS	Conditions ①	Minimum	Typical/Nominal	Maximum	Units
Input Voltage, Continuous	Full temperature range	0		80	Vdc
Input Voltage, Transient	Operating or non-operating, tested: 100 mS max. duration	0		100	Vdc
Isolation Voltage	Input to output			2250	Vdc
Input Reverse Polarity	None, install external fuse		None		Vdc
On/Off Remote Control	Power on, referred to -Vin	0		15	Vdc
Output Power		0		50.63	W
Output Current	Current-limited, no damage, short-circuit protected	0		10	A
Storage Temperature Range	Vin = Zero (no power)	-55		125	°C
Absolute maximums are stress ratings. Exposure of devices to greater than any of these conditions may adversely affect long-term reliability. Proper operation under conditions other than those listed in the Performance/Functional Specifications Table is not implied or recommended.					
INPUT					
Operating voltage range		18	48	75	Vdc
Recommended External Fuse	Fast blow			5	A
Start-up threshold, turn on	Rising input voltage	16.5	17.50	17.9	Vdc
Undervoltage shutdown, turn off	Falling input voltage	15.0	16.75	17.5	Vdc
Overvoltage shutdown			NA		Vdc
Reverse Polarity Protection	None, install external fuse		None		Vdc
Internal Filter Type			LC		
Input current					
Full Load Conditions	Vin = nominal		1.14	1.20	A
Low Line	Vin = minimum		2.44	2.51	A
Inrush Transient			0.4		A2-Sec.
Output in Short Circuit			100	200	mA
No Load Input Current	Iout = minimum, unit=ON		30	60	mA
Shut-Down Mode Input Current			5	10	mA
Reflected (back) ripple current ②	No filtering		150	200	mAp-p
Reflected (back) ripple current ②	Measured at input with specified filter		15	30	mAp-p
Pre-biased startup	External output voltage < Vset		Monotonic		
GENERAL and SAFETY					
Efficiency	Vin=48V, full load	88.0	91.0		%
	Vin=24V, full load	89.5	91.0		%
Isolation					
Isolation Voltage, Input to Output		2250			Vdc
Insulation Safety Rating			Basic		
Isolation Resistance			100		MΩ
Isolation Capacitance			1000		pF
Safety (meets the following requirements)	UL-60950-1, CSA-C22.2 No.60950-1, IEC60950-1, 2nd Edition		Yes		
Calculated MTBF	Per Telcordia SR332, issue 1, class 3, ground fixed, Tambient=+25°C		3.0		Hours x 10 ⁶
DYNAMIC CHARACTERISTICS					
Fixed Switching Frequency		225	275	325	kHz
Startup Time	Power On to Vout regulated			30	mS
Startup Time	Remote ON to Vout regulated			30	mS
Dynamic Load Response	50-75-50% load step, settling time to within ±1% of Vout		100	200	µSec
Dynamic Load Peak Deviation	Same as above,		±180	±240	mV
FEATURES and OPTIONS					
Remote On/Off Control ⑥					
"N" suffix					
Negative Logic, ON state	ON = Pin grounded or external voltage	-0.1		0.8	V
Negative Logic, OFF state	OFF = Pin open or external voltage	2.5		15	V
Control Current	open collector/drain		1	2	mA
"P" suffix					
Positive Logic, ON state	ON = Pin open or external voltage	10		15	V
Positive Logic, OFF state	OFF = Ground pin or external voltage	0		0.7	V
Control Current	open collector/drain		1	2	mA

FUNCTIONAL SPECIFICATIONS, UWS-5/10-Q48 (CONT.)

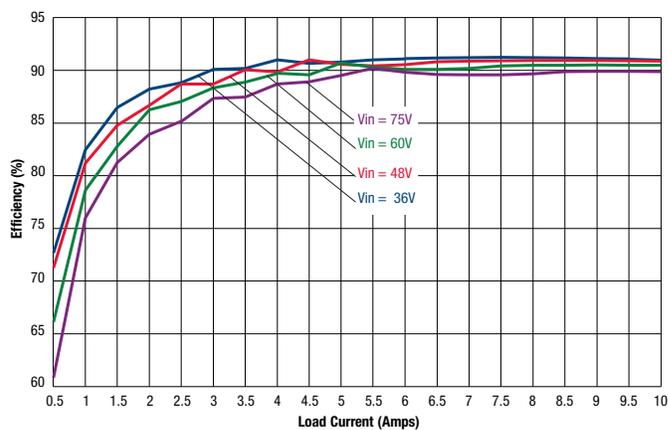
OUTPUT	Conditions ①	Minimum	Typical/Nominal	Maximum	Units
Total Output Power	See Derating	0.0	50	50.63	W
Voltage					
Nominal Output Voltage	No trim	4.938	5.0	5.063	Vdc
Setting Accuracy	At 50% load	-1.25		1.25	% of Vset
Output Voltage Range	User-adjustable	-20		10	
Overvoltage Protection	Via magnetic feedback	6.2	6.4	6.6	Vdc
Current					
Output Current Range	Vin=18V to 36V	0		8	
Output Current Range	Vin=36V to 75V	0		10	A
Minimum Load			No minimum load		
Current Limit Inception	98% of Vnom., cold condition	11.0	13.0	15.5	A
Short Circuit					
Short Circuit Current	Hiccup technique, autorecovery within ±1% of Vout		0.6		A
Short Circuit Duration (remove short for recovery)	Output shorted to ground, no damage		Continuous		
Short circuit protection method	Current limiting				
Regulation ⑦					
Line Regulation	Vin=min. to max., Vout=nom., nom load			±0.125	%
Load Regulation	Iout=min. to max			±0.125	%
Ripple and Noise ⑯	With a 1uF 10 uF output caps.		90		mV pk-pk
	With a 1uF 100uF output caps		65		mV pk-pk
Temperature Coefficient	At all outputs		0.02		% of Vout./°C
Remote Sense Compensation ⑰	Sense connected at load			10	% of Vout
Maximum Capacitive Loading (10% ceramic, 90% Oscon)	Low ESR	0	5000		μF
MECHANICAL					
Outline Dimensions			1.30x0.90x0.36		Inches
(Please refer to outline drawing)	LxWxH		33.0x22.9x9.1		mm
Weight			0.48		Ounces
			13.6		Grams
Through Hole Pin Diameter	Diameter of pins standard		0.040 & 0.060		Inches
			1.02 & 1.52		mm
Through Hole Pin Material			Gold-plated copper alloy with nickel underplate		
TH Pin Plating Metal and Thickness	Nickel subplate		50		μ-inches
	Gold overplate		5		μ-inches
EMI/RFI Shielding			none		
ENVIRONMENTAL					
Operating Ambient Temperature Range	See derating curves	-40		85	°C
Storage Temperature	Vin = Zero (no power)	-55		125	°C
Operating Case Temp	No derating required	-40		105	°C
Thermal Protection/Shutdown	Measured at hotspot	115	125	130	°C
Electromagnetic Interference Conducted, EN55022/CISPR22	External filter is required		A		Class

TYPICAL PERFORMANCE DATA, UWS-5/10-Q48

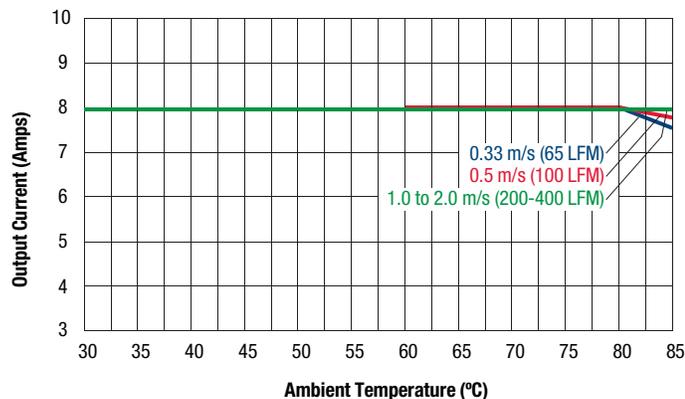
Efficiency vs. Line Voltage and Load Current @ 25°C – Vin = 18V-36V



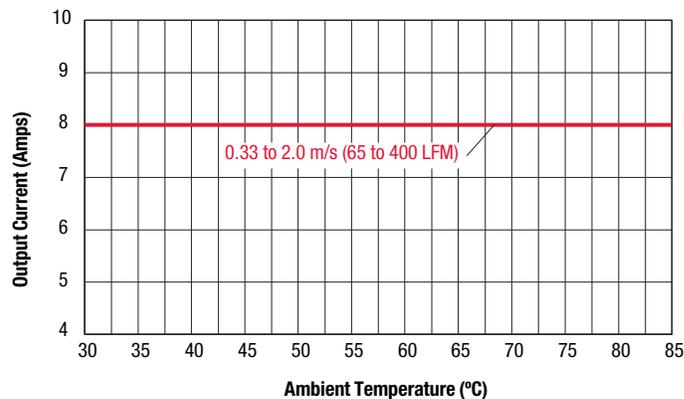
Efficiency vs. Line Voltage and Load Current @ 25°C – Vin = 36V-75V



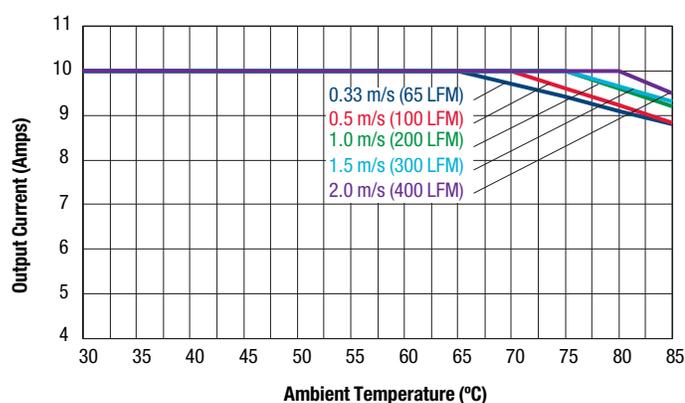
Maximum Current Temperature Derating at Sea Level
(Vin = 18, airflow is from pin 1 to pin 3)



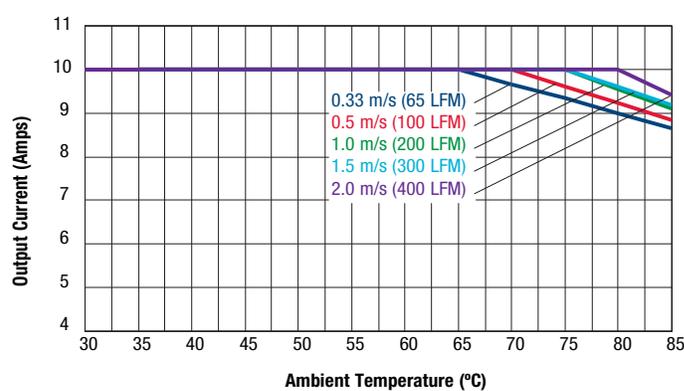
Maximum Current Temperature Derating at Sea Level
(Vin = 24, airflow is from pin 1 to pin 3)



Maximum Current Temperature Derating at Sea Level
(Vin = 36, airflow is from pin 1 to pin 3)

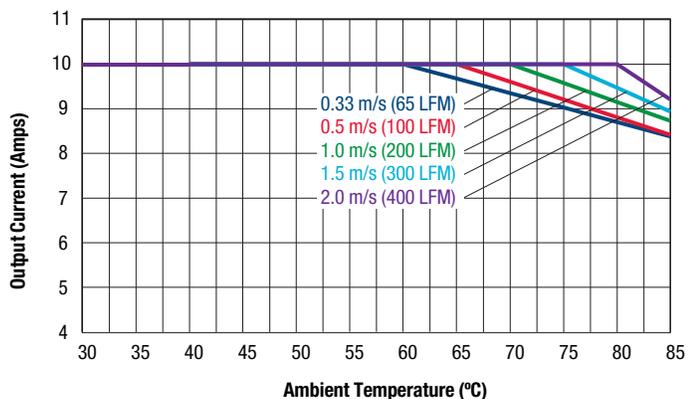


Maximum Current Temperature Derating at Sea Level
(Vin = 48V, airflow is from pin 3 to pin 1)

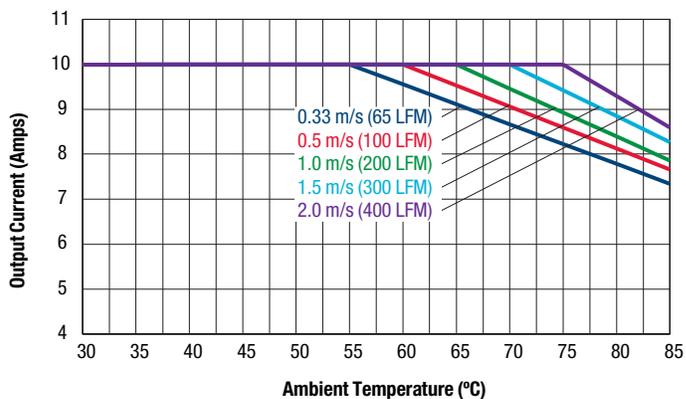


TYPICAL PERFORMANCE DATA, UWS-5/10-Q48

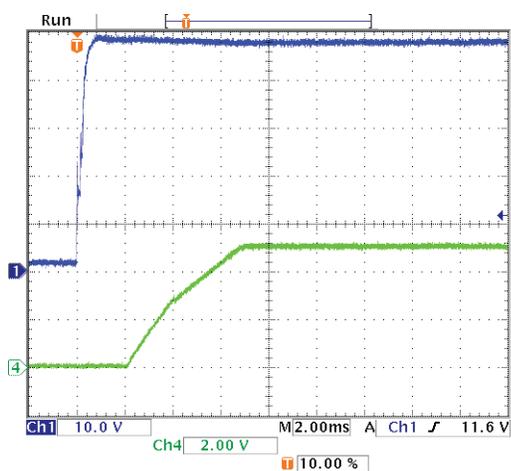
Maximum Current Temperature Derating at Sea Level
($V_{IN} = 60$, airflow is from pin 1 to pin 3)



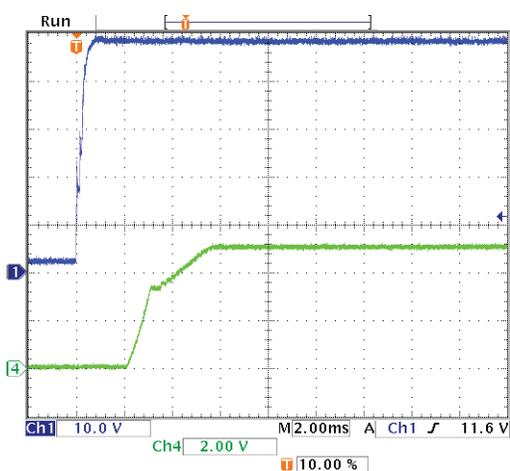
Maximum Current Temperature Derating at Sea Level
($V_{IN} = 75$, airflow is from pin 1 to pin 3)



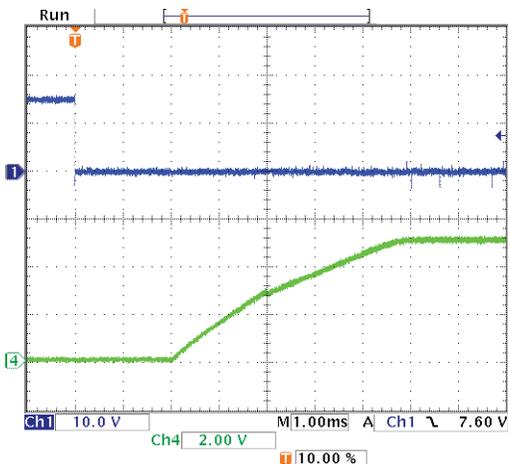
Power On Startup Delay ($V_{in}=0$ to 48V, $V_{out}=nom$, $I_{out}=10A$, $C_{load}=0$ uF, $T_a=+25^\circ C$.,
ScopeBW=20MHz) Trace 1=Vin, Trace 4=Vout



Power On Startup Delay ($V_{in}=0$ to 48V, $V_{out}=nom$, $I_{out}=0A$, $C_{load}=0$ uF, $T_a=+25^\circ C$.,
ScopeBW=20MHz) Trace 1=Vin, Trace 4=Vout

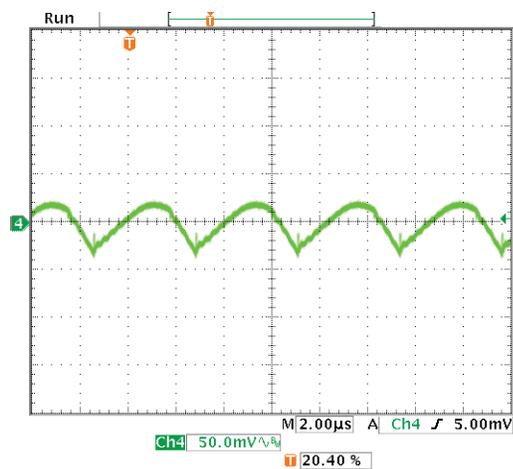


On/Off Enable Delay ($V_{in}=48V$, $V_{out}=nom$, $I_{out}=10A$, $C_{load}=0$ uF, $T_a=+25^\circ C$.,
ScopeBW=20MHz) Trace 1=Enable, Trace 4=Vout

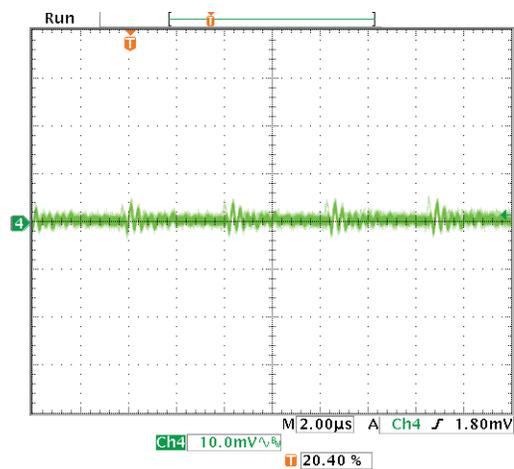


TYPICAL PERFORMANCE DATA, UWS-5/10-Q48

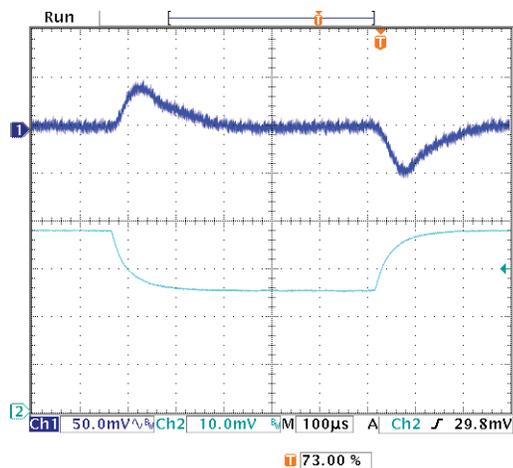
Output Ripple and Noise (Vin=48V, Vout=nom., Iout=10A, Cload=1 uFceramic II 10uF tantalum, Ta=+25°C., ScopeBW=20MHz)



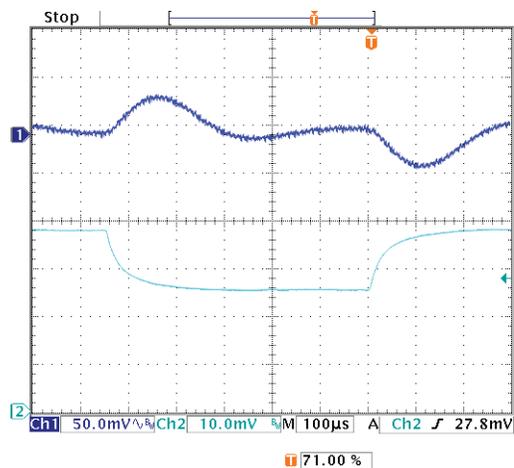
Output Ripple and Noise (Vin=48V, Vout=nom., Iout=0A, Cload=1 uF ceramic II 10uF tantalum, Ta=+25°C., ScopeBW=20MHz)



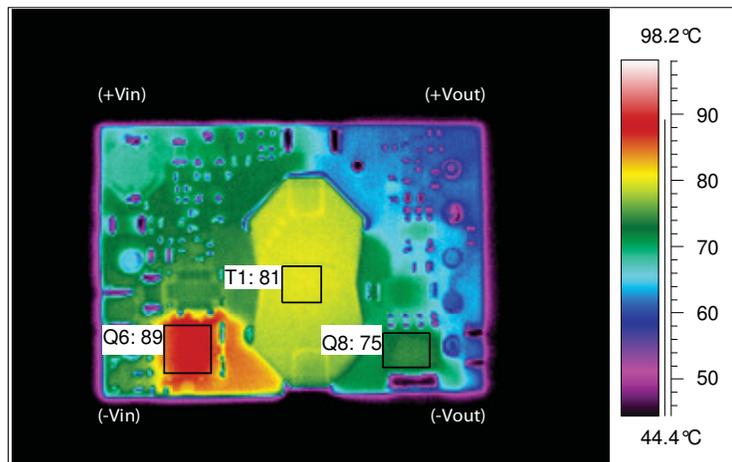
Step Load Transient Response (Vin=48V, Vout=nom., Iout=50-75-50% of full load, Cload=1 uF ceramic II 10uF tantalum, Ta=+25°C., ScopeBW=20MHz Ch1=5Vo, Ch2=Io 2amps/div)



Step Load Transient Response (Vin=48V, Vout=nom., Iout=50-75-50% of full load, Cload=680uF, Ta=+25°C., ScopeBW=20MHz Ch1=5Vo, Ch2=Io 2amps/div)



Thermal image with hot spot at full load current with 25 °C ambient temperature. Natural convection is used with no forced airflow. Identifiable and recommended maximum value to be verified in application. Vin=48V, Q6 max Temp=120 °C/IPC9592 guidelines.



FUNCTIONAL SPECIFICATIONS, UWS-12/4.5-Q48

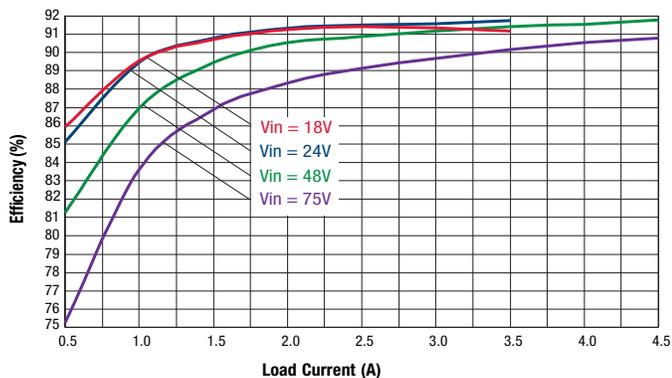
ABSOLUTE MAXIMUM RATINGS	Conditions ①	Minimum	Typical/Nominal	Maximum	Units
Input Voltage, Continuous	Full temperature range	0		80	Vdc
Input Voltage, Transient	Operating or non-operating, 100 mS max. duration	0		100	Vdc
Isolation Voltage	Input to output tested			2250	Vdc
Input Reverse Polarity	None, install external fuse		None		Vdc
On/Off Remote Control	Power on or off, referred to -Vin	0		15	Vdc
Output Power		0		54.54	W
Output Current	Current-limited, no damage, short-circuit protected	0		4.5	A
Storage Temperature Range	Vin = Zero (no power)	-55		125	°C
Absolute maximums are stress ratings. Exposure of devices to greater than any of these conditions may adversely affect long-term reliability. Proper operation under conditions other than those listed in the Performance/Functional Specifications Table is not implied or recommended.					
INPUT					
Operating voltage range		18	48	75	Vdc
Recommended External Fuse	Fast blow		6		A
Start-up threshold	Rising input voltage	16.5	17.2	17.9	Vdc
Undervoltage lockout	Falling input voltage	15.0	16.5	17.5	Vdc
Overvoltage shutdown	Rising input voltage		None		Vdc
Reverse Polarity Protection	None, install external fuse		None		Vdc
Internal Filter Type			LC		
Input current					
Full Load Conditions	Vin = nominal		1.24	1.28	A
Low Line	Vin = minimum , 3.5A load		2.55	2.63	A
Inrush Transient			0.05		A2-Sec.
Output in Short Circuit			100	200	mA
No Load Input Current	Iout = minimum, unit=ON		25	60	mA
Shut-Down Mode Input Current (Off, UV, OT)			5	10	mA
Reflected (back) ripple current ②	Measured at input with specified filter		30	40	mA, pk-pk
Pre-biased startup	External output voltage < Vset		Monotonic		
GENERAL and SAFETY					
Efficiency	Vin=48V, full load	89.0	91.0		%
	Vin=24V, full load	89.5	91.5		%
Isolation					
Isolation Voltage, Input to Output		2250			Vdc
Insulation Safety Rating			Basic		
Isolation Resistance			100		MΩ
Isolation Capacitance			1000		pF
Safety (Designed to meet the following requirements)	UL-60950-1, IEC60950-1, 2nd Edition		Yes		
Calculated MTBF ④	Per Telcordia SR332, issue 1, class 3, ground fixed, Tambient=+25°C		3.0		Hours x 10 ⁶
DYNAMIC CHARACTERISTICS					
Fixed Switching Frequency		200	230	260	kHz
Power Up Startup Time	Power On to Vout regulated			30	mS
On/Off Startup Time	Remote ON to Vout regulated			30	mS
Dynamic Load Response	50-75-50% load step, settling time to within ±1% of Vout		250	300	μSec
Dynamic Load Peak Deviation	Same as above,		±350	±400	mV
FEATURES and OPTIONS					
Remote On/Off Control ⑥					
"N" suffix					
Negative Logic, ON state	ON=Pin grounded or external voltage	-0.1		0.8	Vdc
Negative Logic, OFF state	OFF=Pin open or external voltage	2.5		15	Vdc
Control Current	Open collector/drain, sourcing		1	2	mA
"P" suffix					
Positive Logic, ON state	ON=Pin open or external voltage	10		15	Vdc
Positive Logic, OFF state	OFF=Pin grounded or external voltage	0		0.7	Vdc
Control Current	Open collector/drain, sinking		1	2	mA

FUNCTIONAL SPECIFICATIONS, UWS-12/4.5-Q48 (CONT.)

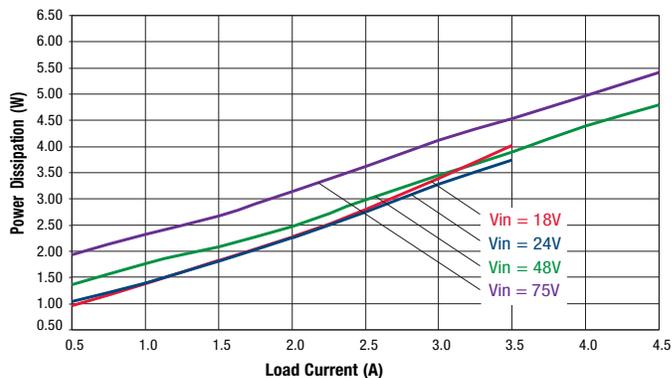
OUTPUT	Conditions ①	Minimum	Typical/Nominal	Maximum	Units
Total Output Power	See Derating	0.0	54.0	54.54	W
Voltage					
Nominal Output Voltage	No trim	11.88	12.00	12.12	Vdc
Setting Accuracy	At 50% load		±1		% of Vnom.
Output Voltage Range	User-adjustable	-20		10	% of Vnom.
Overvoltage Protection	Via magnetic feedback	13.3	15.3	18.0	Vdc
Current					
Output Current Range	Vin=18V-36V	0.0		3.5	A
Output Current Range	Vin=36V-75V	0.0		4.5	A
Minimum Load			No minimum load		
Current Limit Inception	98% of Vnom., after warmup	5.05	6.4	7.4	A
Short Circuit					
Short Circuit Current	Hiccup technique, autorecovery within ±1.25% of Vout		0.6		A
Short Circuit Duration (remove short for recovery)	Output shorted to ground, no damage		Continuous		
Short circuit protection method	Current limiting				
Regulation ⑦					
Line Regulation	Vin=min. to max., Vout=nom., full load			±0.125	%
Load Regulation	Iout=min. to max., Vin=48V			±0.125	%
Ripple and Noise	with a 1uF 10uF output caps		115	150	mV pk-pk
Temperature Coefficient	At all outputs		±0.02		% of Vnom./°C
Maximum Capacitive Load	Constant resistance mode, low ESR	0	2200		µF
MECHANICAL					
Outline Dimensions			1.30x0.90x0.36		Inches
(Please refer to outline drawing)	LxWxH		33.0x22.9x9.1		mm
Weight			0.48		Ounces
			13.6		Grams
Through Hole Pin Diameter			0.040 & 0.060		Inches
			1.02 & 1.52		mm
Through Hole Pin Material			Copper alloy		
TH Pin Plating Metal and Thickness	Nickel subplate		50		µ-inches
	Gold overplate		5		µ-inches
EMI/RFI Shielding			None		
ENVIRONMENTAL					
Operating Ambient Temperature Range	No derating, full power, natural convection	-40		85	°C
Operating Case Temperature Range	No derating, full power, natural convection	-40		105	°C
Storage Temperature	Vin = Zero (no power)	-55		125	°C
Thermal Protection/Shutdown	Measured in center	115	125	130	°C
Electromagnetic Interference Conducted, EN55022/CISPR22	External filter is required		A		Class

TYPICAL PERFORMANCE DATA, UWS-12/4.5-Q48

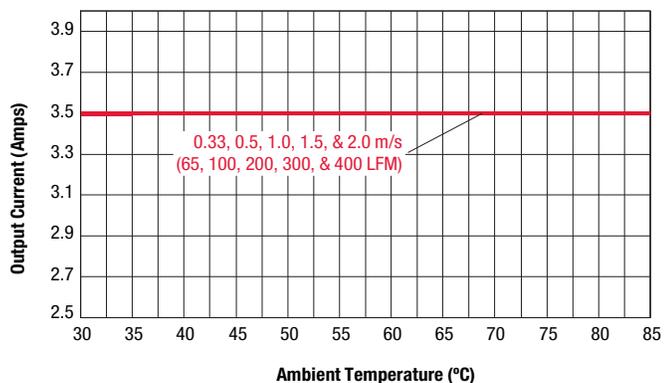
Efficiency vs. Line Voltage and Load Current @ 25°C



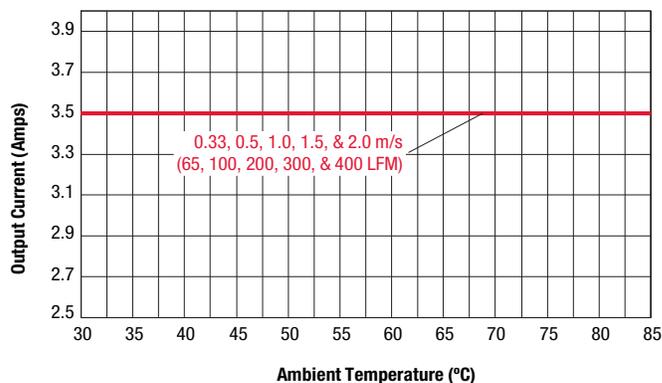
Power Dissipation vs. Load Current @ 25°C



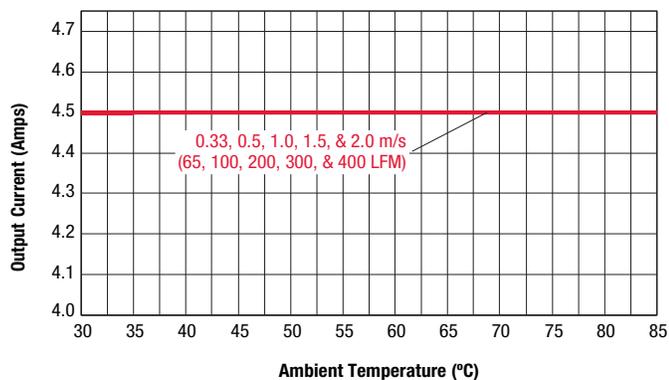
Maximum Current Temperature Derating at Sea Level
(Vin = 18V, air flow from Pin J1 to Pin J3)



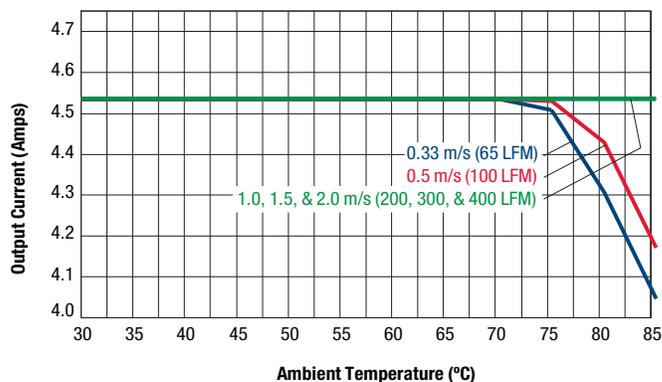
Maximum Current Temperature Derating at Sea Level
(Vin = 24V air flow from Pin J1 to Pin J3 on PCB)



Maximum Current Temperature Derating at Sea Level
(Vin = 48V air flow from Pin J1 to Pin J3 on PCB)

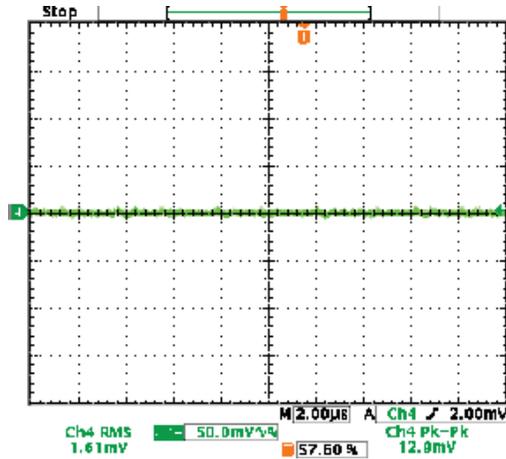


Maximum Current Temperature Derating at Sea Level
(Vin = 75V air flow from Pin J1 to Pin J3 on PCB)

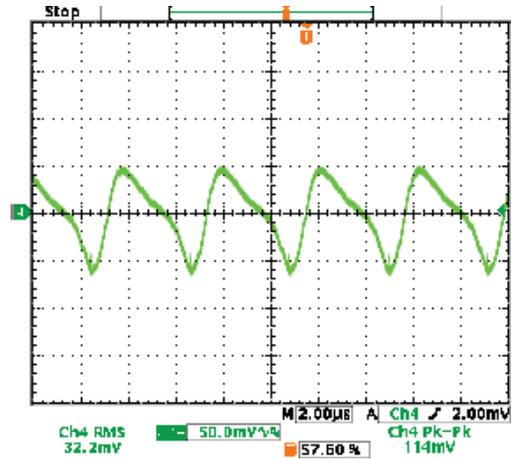


TYPICAL PERFORMANCE DATA, UWS-12/4.5-Q48

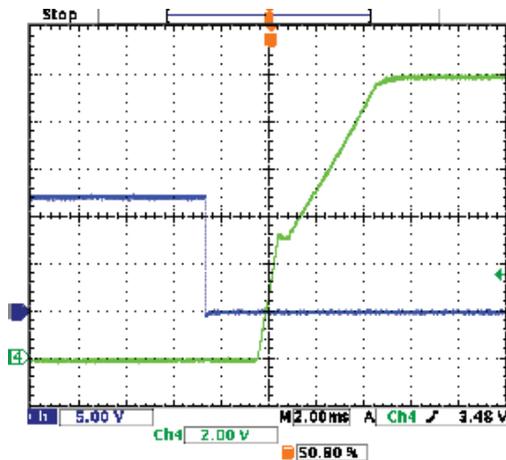
Output Ripple and Noise (Vin=48V, Vout=nom., Iout=no load, Cload=1μF ceramic || 10μF tantalum, Ta=+25°C., ScopeBW=20MHz)



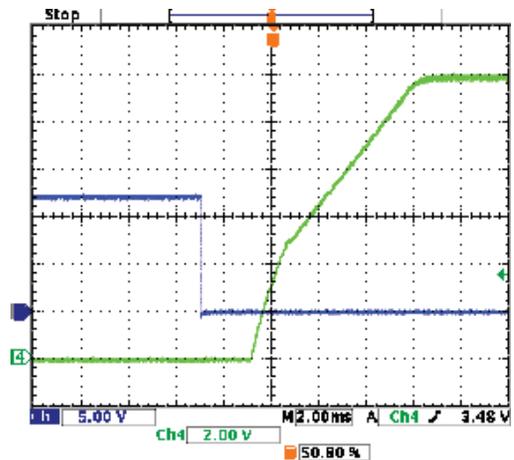
Output Ripple and Noise (Vin=48V, Vout=nom., Iout=4.5A, Cload=1μF ceramic || 10μF tantalum, Ta=+25°C., ScopeBW=20MHz)



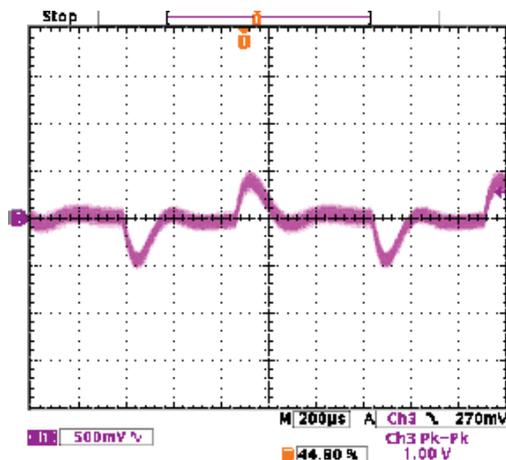
On/Off Enable Delay (Vin=48V, Vout=nom., Iout=no load, Cload=0 μF, Ta=+25°C., ScopeBW=20MHz) Trace 1=Enable, Trace 4=Vout



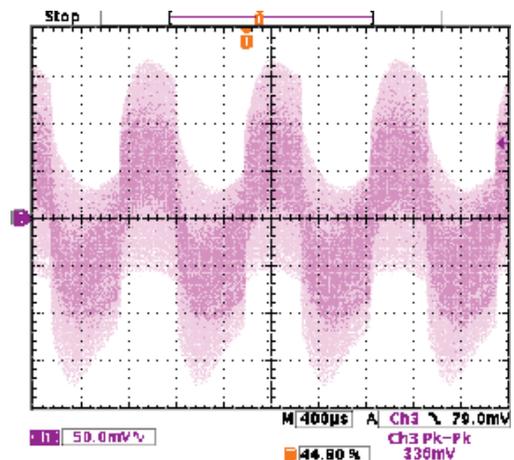
On/Off Enable Delay (Vin=48V, Vout=nom., Iout=4.5A, Cload=0 μF, Ta=+25°C., ScopeBW=20MHz) Trace 1=Enable, Trace 4=Vout



Step Load Transient Response (Vin=48V, Vout=nom., Iout=50-75-50% of full load, Cload=1μF ceramic || 10μF tantalum, Ta=+25°C., ScopeBW=20MHz)

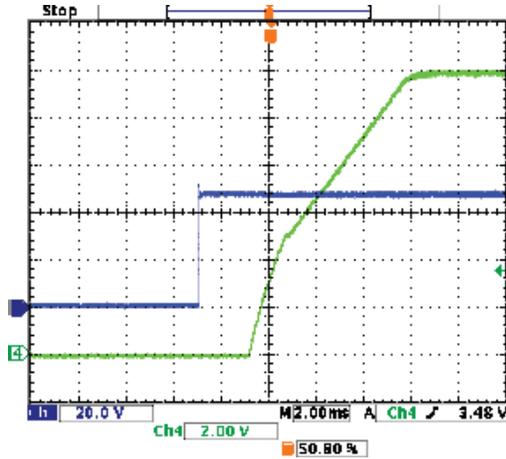


Step Load Transient Response (Vin=48V, Vout=nom., Iout=50-75-50% of full load, Cload=2200 μF, Ta=+25°C., ScopeBW=20MHz)

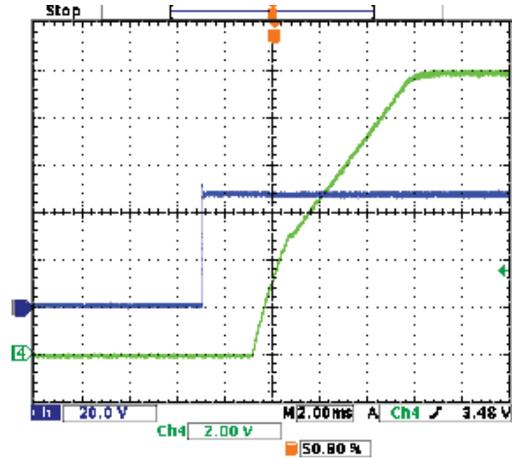


TYPICAL PERFORMANCE DATA, UWS-12/4.5-Q48

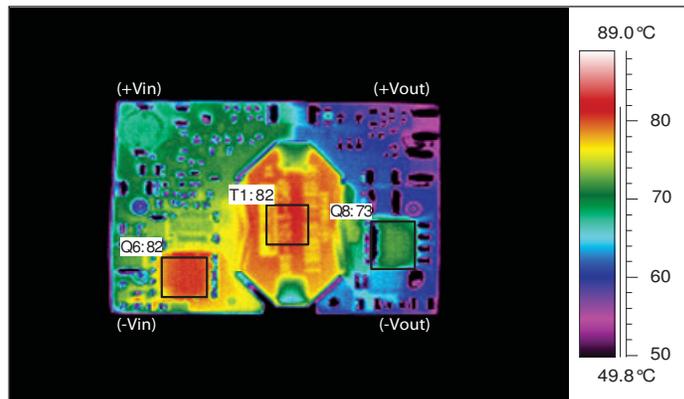
Power On Startup Delay (Vin=0 to 48V, Vout=nom., Iout=no load, Cload=0 μF, Ta=+25°C., ScopeBW=20MHz) Trace 1=Vin, Trace 4=Vout



Power On Startup Delay (Vin=0 to 48V, Vout=nom., Iout=4.5A, Cload=0 μF, Ta=+25°C., ScopeBW=20MHz) Trace 1=Vin, Trace 4=Vout



Thermal image with hot spot at full load current with 25 °C ambient temperature. Natural convection is used with no forced airflow. Identifiable and recommended maximum value to be verified in application. Vin=48V, Q6 and T1 max Temp=120 °C/IPC9592 guidelines.



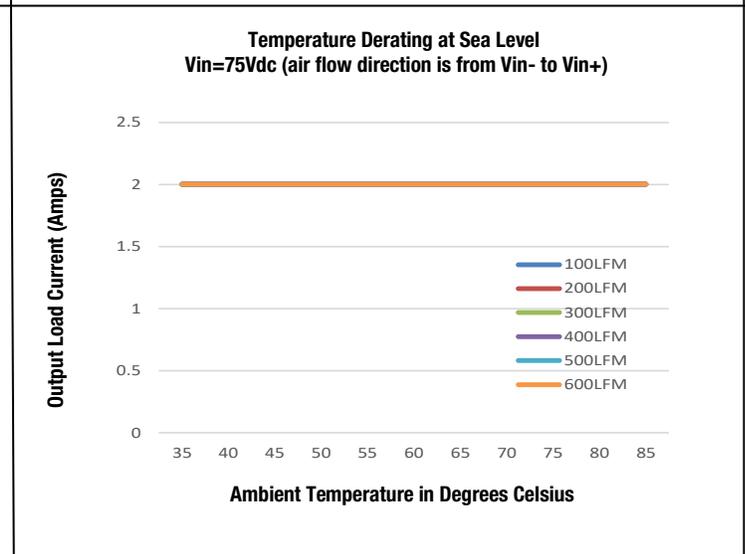
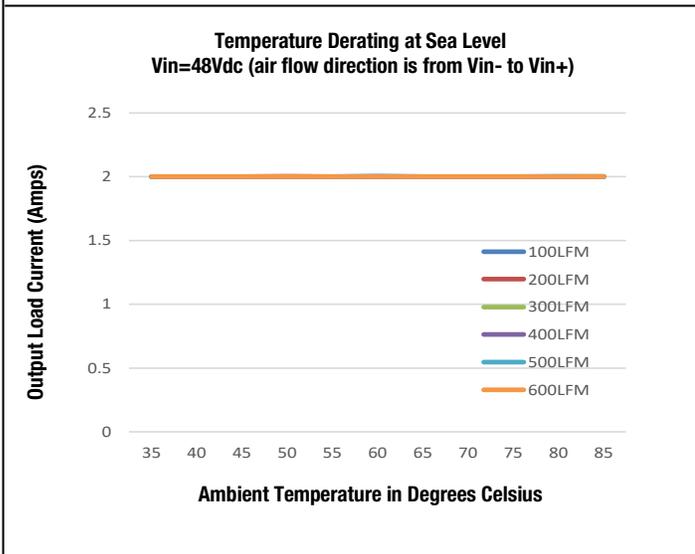
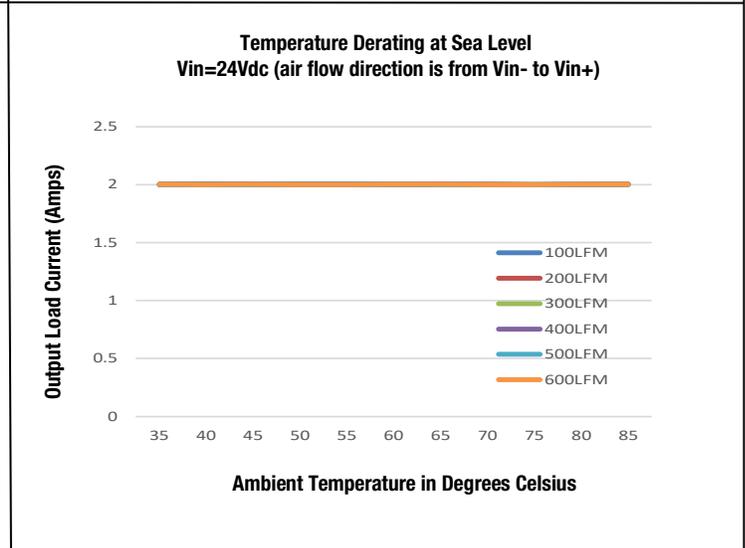
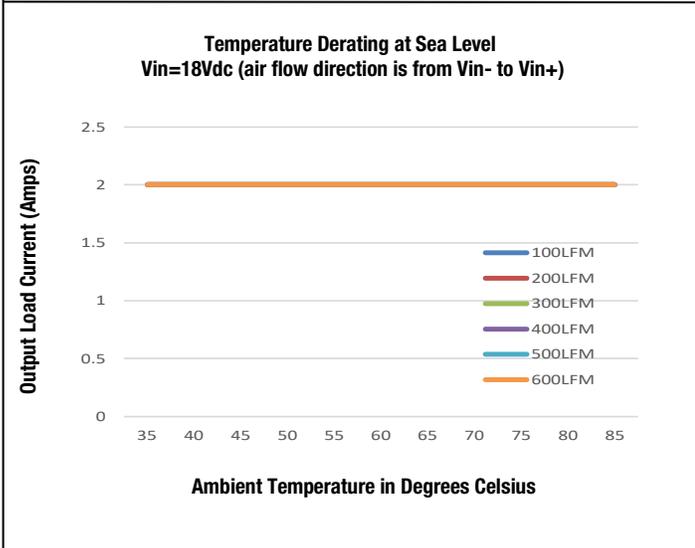
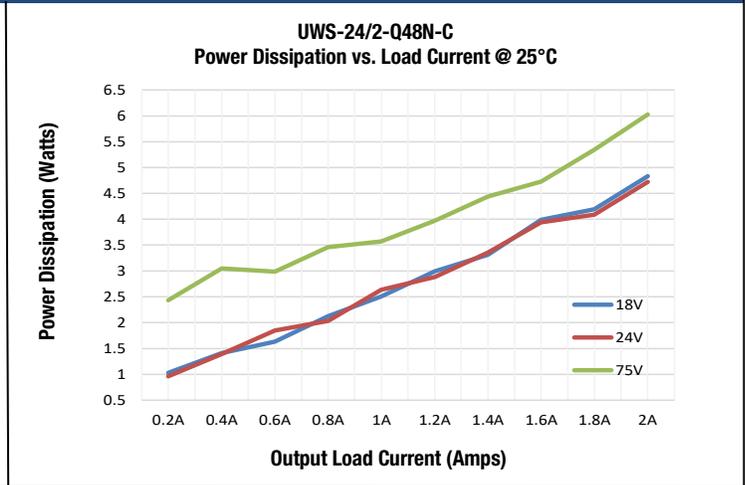
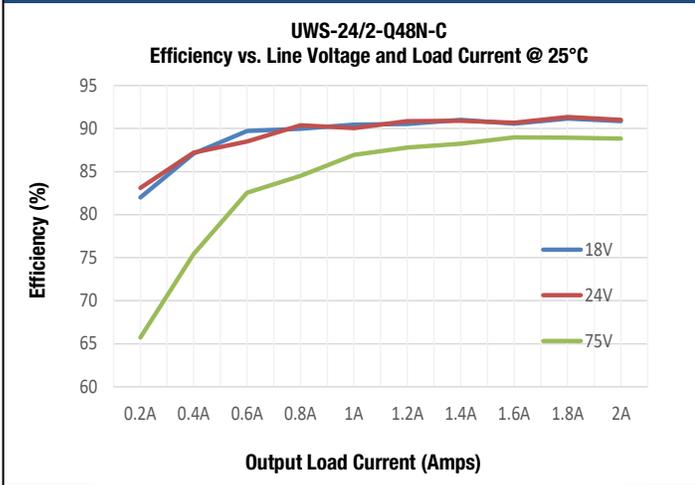
FUNCTIONAL SPECIFICATIONS, UWS-24/2-Q48

ABSOLUTE MAXIMUM RATINGS	Conditions ①	Minimum	Typical/Nominal	Maximum	Units
Input Voltage, Continuous	Full temperature range	0		80	Vdc
Input Voltage, Transient	Operating or non-operating, 100 mS max. duration	0		100	Vdc
Isolation Voltage	Input to output tested			2250	Vdc
Input Reverse Polarity	None, install external fuse		None		Vdc
On/Off Remote Control	Power on or off, referred to -Vin	0		15	Vdc
Output Power		0		48.0	W
Output Current	Current-limited, no damage, short-circuit protected	0		2	A
Storage Temperature Range	Vin = Zero (no power)	-55		125	°C
Absolute maximums are stress ratings. Exposure of devices to greater than any of these conditions may adversely affect long-term reliability. Proper operation under conditions other than those listed in the Performance/Functional Specifications Table is not implied or recommended.					
INPUT					
Operating voltage range		18	48	75	Vdc
Recommended External Fuse	Fast blow			6	A
Start-up threshold	Rising input voltage	16.0	17.2	17.9	Vdc
Undervoltage lockout	Falling input voltage	15.0	16.5	17.5	Vdc
Overvoltage shutdown	Rising input voltage		None		Vdc
Reverse Polarity Protection	None, install external fuse		None		Vdc
Internal Filter Type			LC		
Input current					
Full Load Conditions	Vin = nominal		1.10	1.12	A
Low Line	Vin = minimum, 2A load		2.91	3.01	A
Inrush Transient			0.05		A2-Sec.
Output in Short Circuit			100	200	mA
No Load Input Current	Iout = minimum, unit=ON		25	75	mA
Shut-Down Mode Input Current (Off, UV, OT)			5	10	mA
Reflected (back) ripple current ②	Measured at input with specified filter		30	40	mA, pk-pk
Pre-biased startup	External output voltage < Vset		Monotonic		
GENERAL and SAFETY					
Efficiency	Vin=48V, full load	89.0	90.5		%
	Vin=24V, full load	89.5	91.0		%
Isolation					
Isolation Voltage, Input to Output		2250			Vdc
Insulation Safety Rating			Basic		
Isolation Resistance			100		MΩ
Isolation Capacitance			1000		pF
Safety (Designed to meet the following requirements)	UL-60950-1, IEC60950-1, 2nd Edition		Yes		
Calculated MTBF ④	Per Telcordia SR332, issue 1, class 3, ground fixed, Tambient=+25°C		3.0		Hours x 10 ⁶
DYNAMIC CHARACTERISTICS					
Fixed Switching Frequency		207	230	253	kHz
Power Up Startup Time	Power On to Vout regulated			30	mS
On/Off Startup Time	Remote ON to Vout regulated			30	mS
Dynamic Load Response	50-75-50% load step, settling time to within ±1% of Vout		250	300	μSec
Dynamic Load Peak Deviation	Same as above,		±350	±400	mV
FEATURES and OPTIONS					
Remote On/Off Control ⑥					
"N" suffix					
Negative Logic, ON state	ON=Pin grounded or external voltage	-0.1		0.8	Vdc
Negative Logic, OFF state	OFF=Pin open or external voltage	2.5		15	Vdc
Control Current	Open collector/drain, sourcing		1	2	mA
"P" suffix					
Positive Logic, ON state	ON=Pin open or external voltage	10		15	Vdc
Positive Logic, OFF state	OFF=Pin grounded or external voltage	0		0.7	Vdc
Control Current	Open collector/drain, sinking		1	2	mA

FUNCTIONAL SPECIFICATIONS, UWS-24/2-Q48 (CONT.)

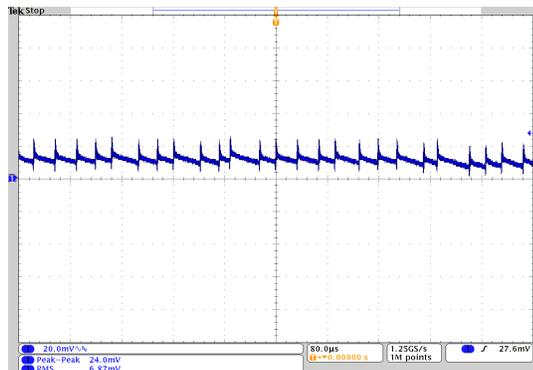
OUTPUT	Conditions ①	Minimum	Typical/Nominal	Maximum	Units
Total Output Power	See Derating	0.0	48.0	48.0	W
Voltage					
Nominal Output Voltage	No trim	23.76	24.00	24.24	Vdc
Setting Accuracy	At 50% load		±1		% of Vnom.
Output Voltage Range	User-adjustable	-20		10	% of Vnom.
Overvoltage Protection	Via magnetic feedback		29	32.5	Vdc
Current					
Output Current Range	Vin=18V-36V	0.0		2	A
Output Current Range	Vin=36V-75V	0.0		2	A
Minimum Load			No minimum load		
Current Limit Inception	98% of Vnom., after warmup	2.75	3.45	4.15	A
Short Circuit					
Short Circuit Current	Hiccup technique, autorecovery within ±1.25% of Vout		0.6		A
Short Circuit Duration (remove short for recovery)	Output shorted to ground, no damage		Continuous		
Short circuit protection method	Current limiting				
Regulation ⑦					
Line Regulation	Vin=min. to max., Vout=nom., full load			±0.125	%
Load Regulation	Iout=min. to max., Vin=48V			±0.125	%
Ripple and Noise	with a 1uF 10uF output caps		200	300	mV pk-pk
Temperature Coefficient	At all outputs		±0.02		% of Vnom./°C
Maximum Capacitive Load	Constant resistance mode, low ESR	0	680		µF
MECHANICAL					
Outline Dimensions			1.30x0.90x0.36		Inches
(Please refer to outline drawing)	LxWxH		33.0x22.9x9.1		mm
Weight			0.48		Ounces
			13.6		Grams
Through Hole Pin Diameter			0.040 & 0.060		Inches
			1.02 & 1.52		mm
Through Hole Pin Material			Copper alloy		
TH Pin Plating Metal and Thickness	Nickel subplate		50		µ-inches
	Gold overplate		5		µ-inches
EMI/RFI Shielding			None		
ENVIRONMENTAL					
Operating Ambient Temperature Range	No derating, full power, natural convection	-40		85	°C
Operating Case Temperature Range	No derating, full power, natural convection	-40		105	°C
Storage Temperature	Vin = Zero (no power)	-55		125	°C
Thermal Protection/Shutdown	Measured in center	115	125	130	°C
Electromagnetic Interference Conducted, EN55022/CISPR22	External filter is required		A		Class

TYPICAL PERFORMANCE DATA, UWS-24/2-Q48

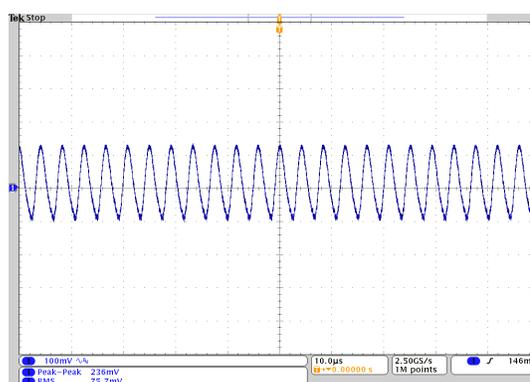


TYPICAL PERFORMANCE DATA, UWS-24/2-Q48

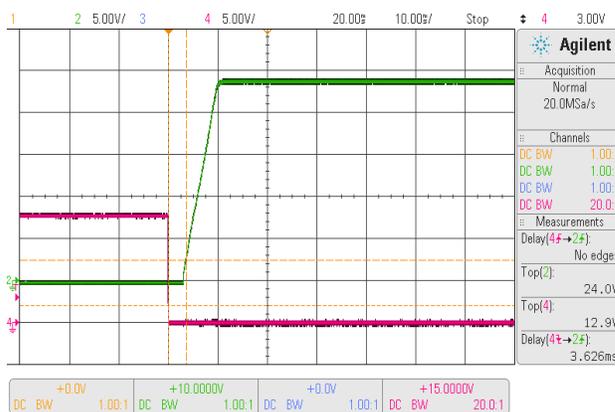
Output Ripple and Noise (Vin=48V, Vout=nom., Iout=no load, Cload=1μF ceramic II 10μF tantalum, Ta=+25°C., ScopeBW=20MHz)



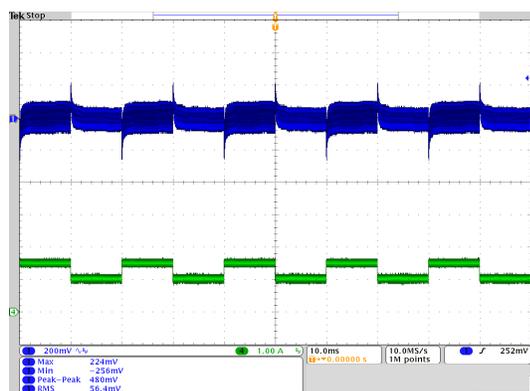
Output Ripple and Noise (Vin=48V, Vout=nom., Iout=2A, Cload=1μF ceramic II 10μF tantalum, Ta=+25°C., ScopeBW=20MHz)



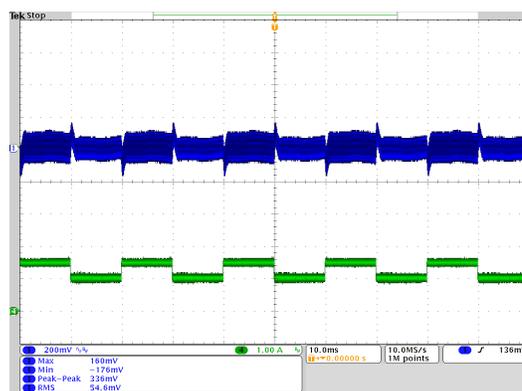
On/Off Enable Delay (Negative logic, Vin=48V, Vout=nom., Iout=2A, Cload=0 μF, Ta=+25°C., ScopeBW=20MHz) Trace 2=Enable, Trace 4=Vout



Step Load Transient Response (Vin=48V, Vout=nom., Iout=50-75-50% of full load, Cload=1μF ceramic II 10μF tantalum, Ta=+25°C., ScopeBW=20MHz)



Step Load Transient Response (Vin=48V, Vout=nom., Iout=50-75-50% of full load, Cload=680μF, Ta=+25°C., ScopeBW=20MHz)



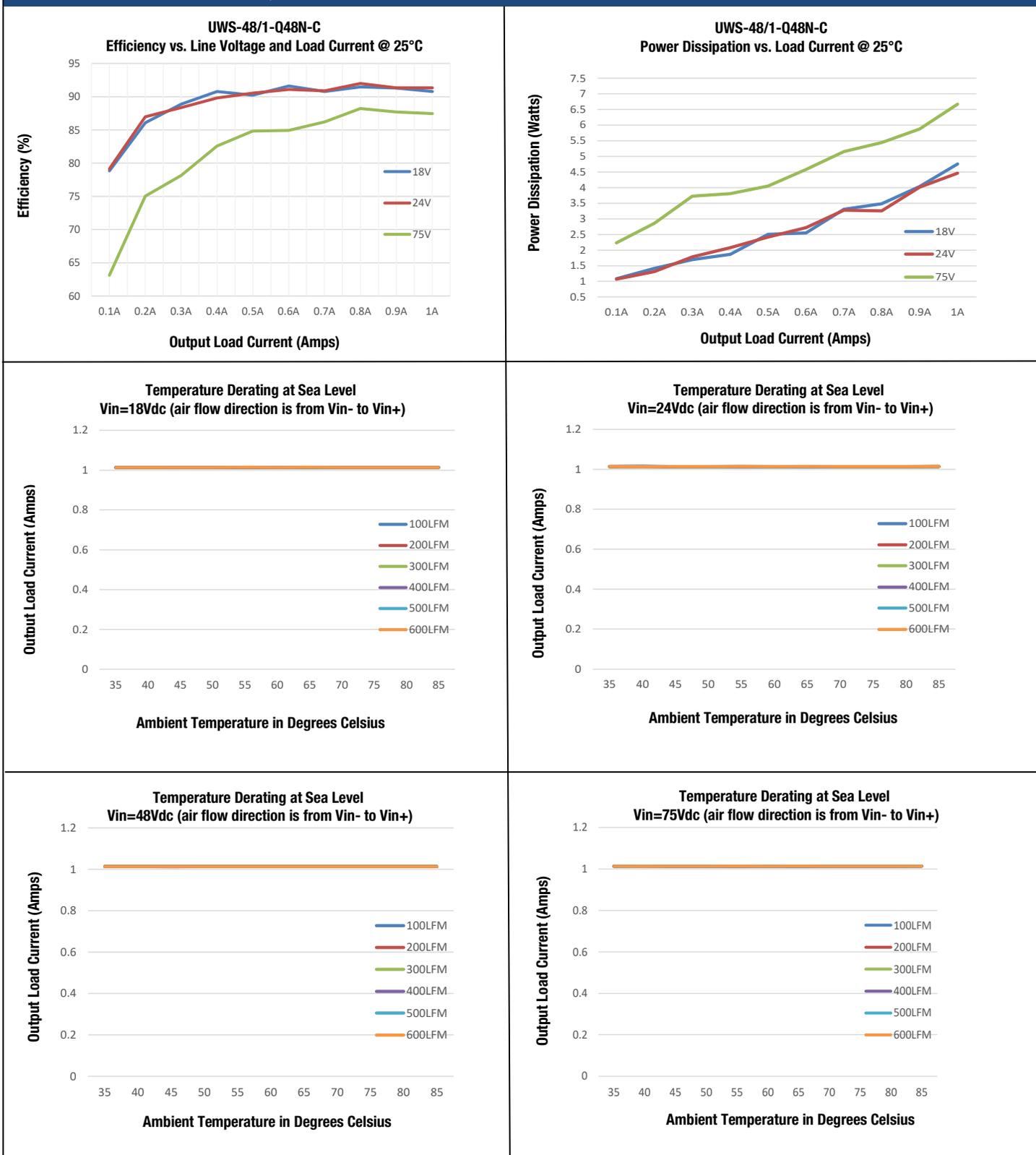
FUNCTIONAL SPECIFICATIONS, UWS-48/1-Q48

ABSOLUTE MAXIMUM RATINGS	Conditions ①	Minimum	Typical/Nominal	Maximum	Units
Input Voltage, Continuous	Full temperature range	0		80	Vdc
Input Voltage, Transient	Operating or non-operating, 100 mS max. duration	0		100	Vdc
Isolation Voltage	Input to output tested			2250	Vdc
Input Reverse Polarity	None, install external fuse		None		Vdc
On/Off Remote Control	Power on or off, referred to -Vin	0		15	Vdc
Output Power		0		48.0	W
Output Current	Current-limited, no damage, short-circuit protected	0		1	A
Storage Temperature Range	Vin = Zero (no power)	-55		125	°C
Absolute maximums are stress ratings. Exposure of devices to greater than any of these conditions may adversely affect long-term reliability. Proper operation under conditions other than those listed in the Performance/Functional Specifications Table is not implied or recommended.					
INPUT					
Operating voltage range		18	48	75	Vdc
Recommended External Fuse	Fast blow			6	A
Start-up threshold	Rising input voltage	16.0	17.2	17.9	Vdc
Undervoltage lockout	Falling input voltage	15.0	16.5	17.5	Vdc
Overvoltage shutdown	Rising input voltage		None		Vdc
Reverse Polarity Protection	None, install external fuse		None		Vdc
Internal Filter Type			LC		
Input current					
Full Load Conditions	Vin = nominal		1.10	1.12	A
Low Line	Vin = minimum, 1A load		2.91	3.01	A
Inrush Transient			0.05		A2-Sec.
Output in Short Circuit			100	200	mA
No Load Input Current	Iout = minimum, unit=ON		25	75	mA
Shut-Down Mode Input Current (Off, UV, OT)			5	10	mA
Reflected (back) ripple current ②	Measured at input with specified filter		30	40	mA, pk-pk
Pre-biased startup	External output voltage < Vset		Monotonic		
GENERAL and SAFETY					
Efficiency	Vin=48V, full load	89.0	91.0		%
	Vin=24V, full load	89.5	91.0		%
Isolation					
Isolation Voltage, Input to Output		2250			Vdc
Insulation Safety Rating			Basic		
Isolation Resistance			100		MΩ
Isolation Capacitance			1000		pF
Safety (Designed to meet the following requirements)	UL-60950-1, IEC60950-1, 2nd Edition		Yes		
Calculated MTBF ④	Per Telcordia SR332, issue 1, class 3, ground fixed, Tambient=+25°C		3.0		Hours x 10 ⁶
DYNAMIC CHARACTERISTICS					
Fixed Switching Frequency		212	236	260	kHz
Power Up Startup Time	Power On to Vout regulated			30	mS
On/Off Startup Time	Remote ON to Vout regulated			30	mS
Dynamic Load Response	50-75-50% load step, settling time to within ±1% of Vout		250	300	μSec
Dynamic Load Peak Deviation	Same as above,		±450	±500	mV
FEATURES and OPTIONS					
Remote On/Off Control ⑥					
"N" suffix					
Negative Logic, ON state	ON=Pin grounded or external voltage	-0.1		0.8	Vdc
Negative Logic, OFF state	OFF=Pin open or external voltage	2.5		15	Vdc
Control Current	Open collector/drain, sourcing		1	2	mA
"P" suffix					
Positive Logic, ON state	ON=Pin open or external voltage	10		15	Vdc
Positive Logic, OFF state	OFF=Pin grounded or external voltage	0		0.7	Vdc
Control Current	Open collector/drain, sinking		1	2	mA

FUNCTIONAL SPECIFICATIONS, UWS-48/1-Q48 (CONT.)

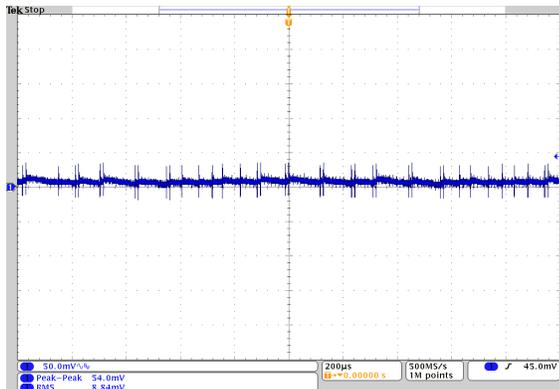
OUTPUT	Conditions ①	Minimum	Typical/Nominal	Maximum	Units
Total Output Power	See Derating	0.0	48.0	48.0	W
Voltage					
Nominal Output Voltage	No trim	47.52	48.00	48.48	Vdc
Setting Accuracy	At 50% load		±1		% of Vnom.
Output Voltage Range	User-adjustable	-20		10	% of Vnom.
Overvoltage Protection	Via magnetic feedback		60	67	Vdc
Current					
Output Current Range	Vin=18V-36V	0.0		1	A
Output Current Range	Vin=36V-75V	0.0		1	A
Minimum Load			No minimum load		
Current Limit Inception	98% of Vnom., after warmup	1.40	1.80	2.10	A
Short Circuit					
Short Circuit Current	Hiccup technique, autorecovery within ±1.25% of Vout		0.6		A
Short Circuit Duration (remove short for recovery)	Output shorted to ground, no damage		Continuous		
Short circuit protection method	Current limiting				
Regulation ⑦					
Line Regulation	Vin=min. to max., Vout=nom., full load			±0.125	%
Load Regulation	Iout=min. to max., Vin=48V			±0.125	%
Ripple and Noise	with a 1uF 10uF output caps		300	400	mV pk-pk
Temperature Coefficient	At all outputs		±0.02		% of Vnom./°C
Maximum Capacitive Load	Constant resistance mode, low ESR	0	330		µF
MECHANICAL					
Outline Dimensions			1.30x0.90x0.36		Inches
(Please refer to outline drawing)	LxWxH		33.0x22.9x9.1		mm
Weight			0.48		Ounces
			13.6		Grams
Through Hole Pin Diameter			0.040 & 0.060		Inches
			1.02 & 1.52		mm
Through Hole Pin Material			Copper alloy		
TH Pin Plating Metal and Thickness	Nickel subplate		50		µ-inches
	Gold overplate		5		µ-inches
EMI/RFI Shielding			None		
ENVIRONMENTAL					
Operating Ambient Temperature Range	No derating, full power, natural convection	-40		85	°C
Operating Case Temperature Range	No derating, full power, natural convection	-40		105	°C
Storage Temperature	Vin = Zero (no power)	-55		125	°C
Thermal Protection/Shutdown	Measured in center	115	125	130	°C
Electromagnetic Interference Conducted, EN55022/CISPR22	External filter is required		A		Class

TYPICAL PERFORMANCE DATA, UWS-48/1-Q48

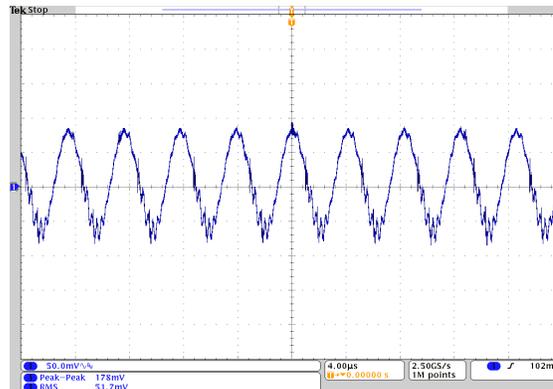


TYPICAL PERFORMANCE DATA, UWS-48/1-Q48

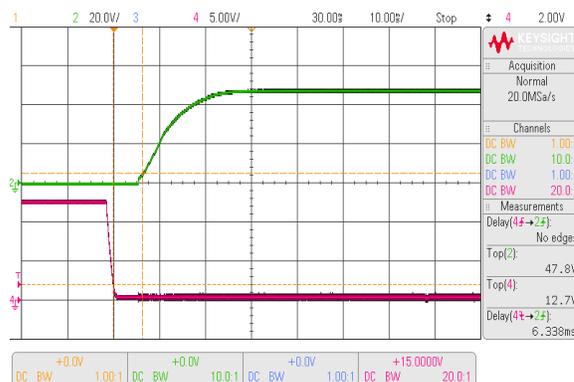
Output Ripple and Noise (Vin=48V, Vout=nom., Iout=no load, Cload=1μF ceramic II 10μF tantalum, Ta=+25°C., ScopeBW=20MHz)



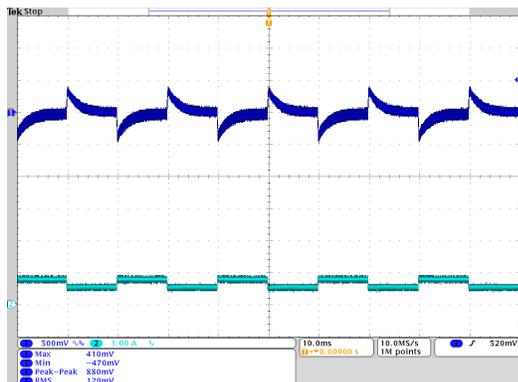
Output Ripple and Noise (Vin=48V, Vout=nom., Iout=1A, Cload=1μF ceramic II 10μF tantalum, Ta=+25°C., ScopeBW=20MHz)



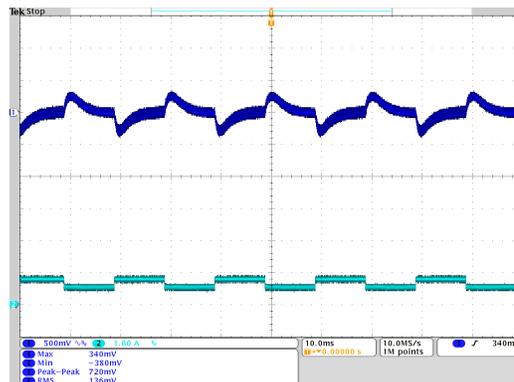
On/Off Enable Delay (Negative logic, Vin=48V, Vout=nom., Iout=1A, Cload=0 μF, Ta=+25°C., ScopeBW=20MHz) Trace 2=Enable, Trace 4=Vout



Step Load Transient Response (Vin=48V, Vout=nom., Iout=50-75-50% of full load, Cload=1μF ceramic II 10μF tantalum, Ta=+25°C., ScopeBW=20MHz)



Step Load Transient Response (Vin=48V, Vout=nom., Iout=50-75-50% of full load, Cload=330μF, Ta=+25°C., ScopeBW=20MHz)



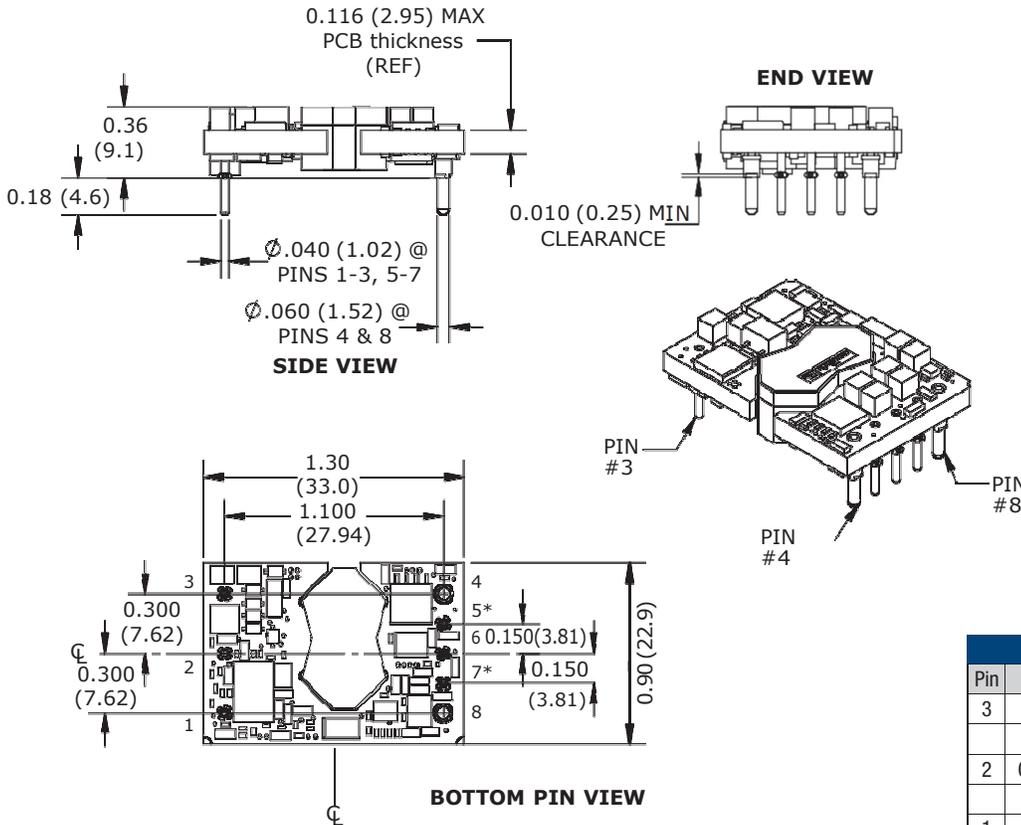
Performance Specification Notes

1. All specifications are typical unless noted. Ambient temperature = +25°Celsius, V_{IN} is nominal, output current is maximum rated nominal. External output capacitance is 1 μ F multilayer ceramic paralleled with 10 μ F electrolytic. All caps are low ESR. These capacitors are necessary for our test equipment and may not be needed in your application. Testing must be kept short enough that the converter does not appreciably heat up during testing. For extended testing, use plenty of airflow. See Derating Curves for temperature performance. All models are stable and regulate within spec without external capacitance.
2. Input Ripple Current is tested and specified over a 5-20 MHz bandwidth and uses a special set of external filters only for the Ripple Current specifications. Input filtering is $C_{IN} = 33 \mu\text{F}$, $C_{BUS} = 220 \mu\text{F}$, $L_{BUS} = 12 \mu\text{H}$. Use capacitor rated voltages which are twice the maximum expected voltage. Capacitors must accept high speed AC switching currents.
3. Note that Maximum Current Derating Curves indicate an average current at nominal input voltage. At higher temperatures and/or lower airflow, the converter will tolerate brief full current outputs if the average RMS current over time does not exceed the Derating curve. All Derating curves are presented at sea level altitude. Be aware of reduced power dissipation with increasing density altitude.
4. Mean Time Before Failure (MTBF) is calculated using the Telcordia (Belcore) SR-332 Method 1, Case 3, Issue 1, ground fixed conditions. Operating temperature = +30°C, full output load, natural air convection.
5. The output may be shorted to ground indefinitely with no damage. The Output Short Circuit Current shown in the specifications is an average consisting of very short bursts of full rated current to test whether the output circuit can be repowered.
6. The On/Off Control is normally driven from a switch or relay. An open collector/open drain transistor may be used in saturation and cut-off (pinch-off) modes. External logic may also be used if voltage levels are fully compliant to the specifications.
7. Regulation specifications describe the deviation as the input line voltage or output load current is varied from a nominal midpoint value to either extreme (50% load).
8. Do not exceed maximum power ratings or output overvoltage when adjusting output trim values.
9. At zero output current, V_{out} may contain components which slightly exceed the ripple and noise specifications.
10. Output overload protection is non-latching. When the output overload is removed, the output will automatically recover.
11. All models are fully operational and meet published specifications, including "cold start" at -40°C.
12. The converter will shut off if the input falls below the undervoltage threshold. It will not restart until the input exceeds the Input Start Up Voltage.
13. Short circuit shutdown begins when the output voltage degrades approximately 2% from the selected setting.
14. Output noise may be further reduced by installing an external filter. See the Application Notes. Use only as much output filtering as needed *and no more*. Larger caps (especially low-ESR ceramic types) may slow transient response or degrade dynamic performance. Thoroughly test your application with all components installed.
15. To avoid damage or unplanned shutdown, do not sink appreciable reverse output current.
16. If reverse polarity is accidentally applied to the input, to ensure reverse input protection with full output load, always connect an external fast blow input fuse in series with the + V_{IN} input.
17. Although extremely unlikely, failure of the internal components of this product may expose external application circuits to dangerous voltages, currents, temperatures or power levels. Please thoroughly verify all applications before committing them to service. Be sure to include appropriately-rated FUSES (see specifications and Application Notes) to reduce the risk of failure.
18. If remote sense is not used, connect it to its respective V_{out} terminal. Sense is included on UWS-3.3/15-Q48 and UWS-5/10-Q48 models only.
19. Output Ripple and Noise for the UWS-5/10-Q48 model with a 1 μ F and 100 μ F Tantalum Output Capacitor is 65mVp-p (Typical).

MECHANICAL SPECIFICATIONS, THROUGH-HOLE MOUNT

Material:

- Ø .040 Pins: copper alloy
- Ø .060 Pins: copper alloy
- Finish: (all pins)
- Gold (5u"min) over nickel (50u" min)



INPUT/OUTPUT CONNECTIONS			
Pin	Function	Pin	Function
3	-Vin	4	-Vout
		5	-Sense*
2	On/Off Control	6	Output Trim
		7	+Sense*
1	+Vin	8	+Vout

* Sense is included only on models UWS-3.3/15-Q48 and UWS-5/10-Q48. Sense pins are omitted on other models.

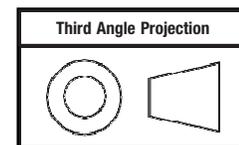
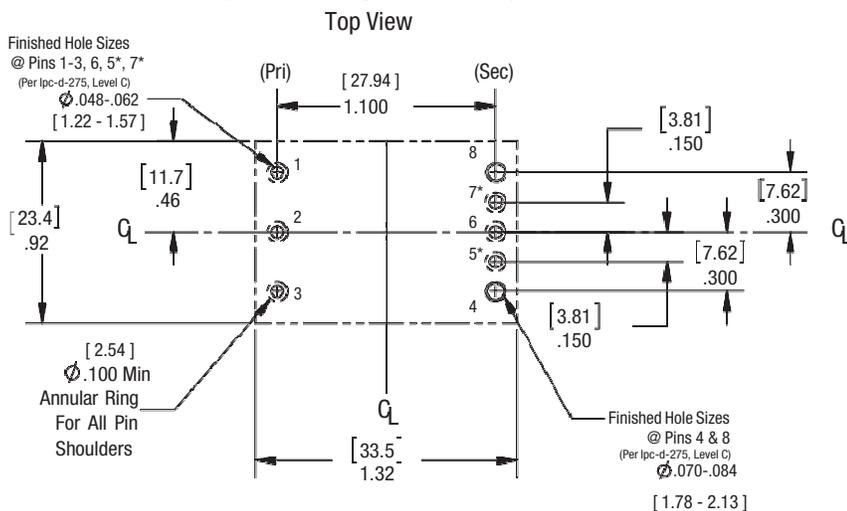
Note that some competitive units may use different pin numbering or alternate outline views. However, all units are pinout compatible.

Standard pin length is shown. Please refer to the part number structure for alternate pin lengths.

It is recommended that no parts be placed beneath the converter.

Dimensions are in inches (mm) shown for ref. only.

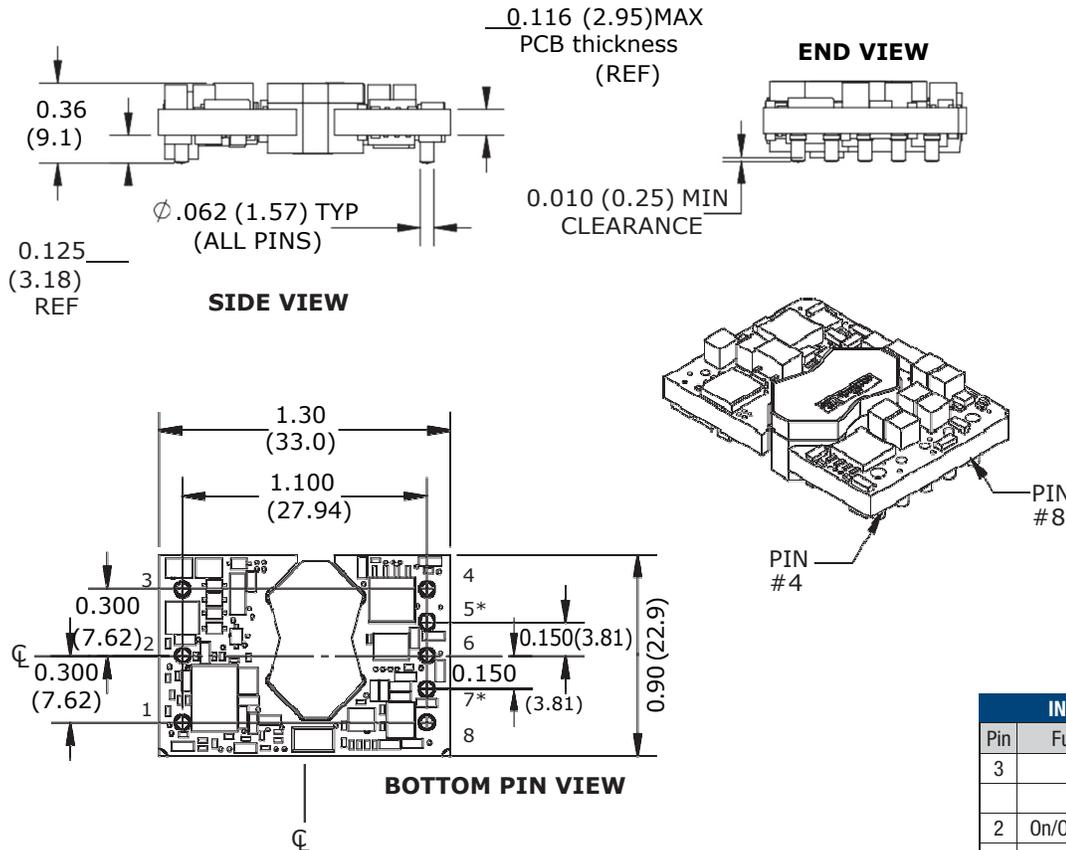
Recommended Footprint For Thru-Hole Converter (View Through Converter)



Tolerances (unless otherwise specified):
 .XX ± 0.02 (0.5)
 .XXX ± 0.010 (0.25)
 Angles ± 1°

Components are shown for reference only and may vary between units.

MECHANICAL SPECIFICATIONS, SURFACE MOUNT (MSL RATING 2)



Material:
SMT contacts: copper alloy
Finish: (all pins)
Gold (5u"min) over nickel (50u" min)

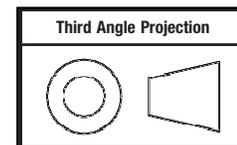
INPUT/OUTPUT CONNECTIONS			
Pin	Function	Pin	Function
3	-Vin	4	-Vout
		5	-Sense*
2	On/Off Control	6	Output Trim
		7	+Sense*
1	+Vin	8	+Vout

* Sense is included only on models UWS-3.3/15-Q48 and UWS-5/10-Q48. Sense pins are omitted on other models.

Note that some competitive units may use different pin numbering or alternate outline views. However, all units are pinout compatible.

It is recommended that no parts be placed beneath the converter.

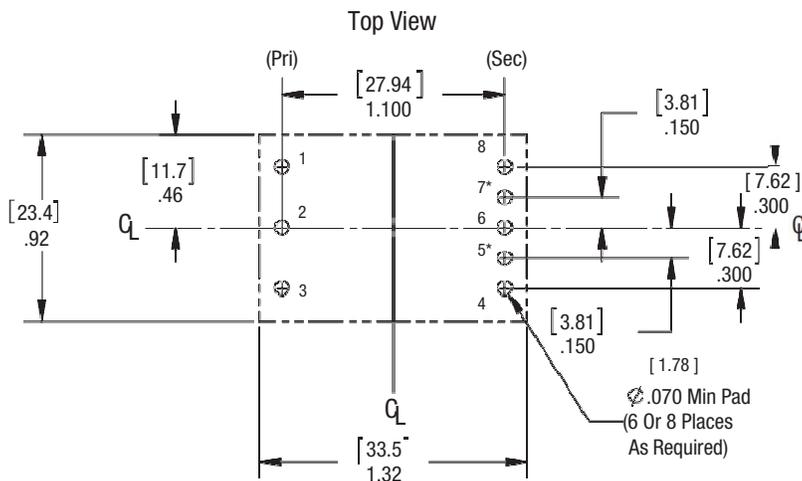
Dimensions are in inches (mm) shown for ref. only.



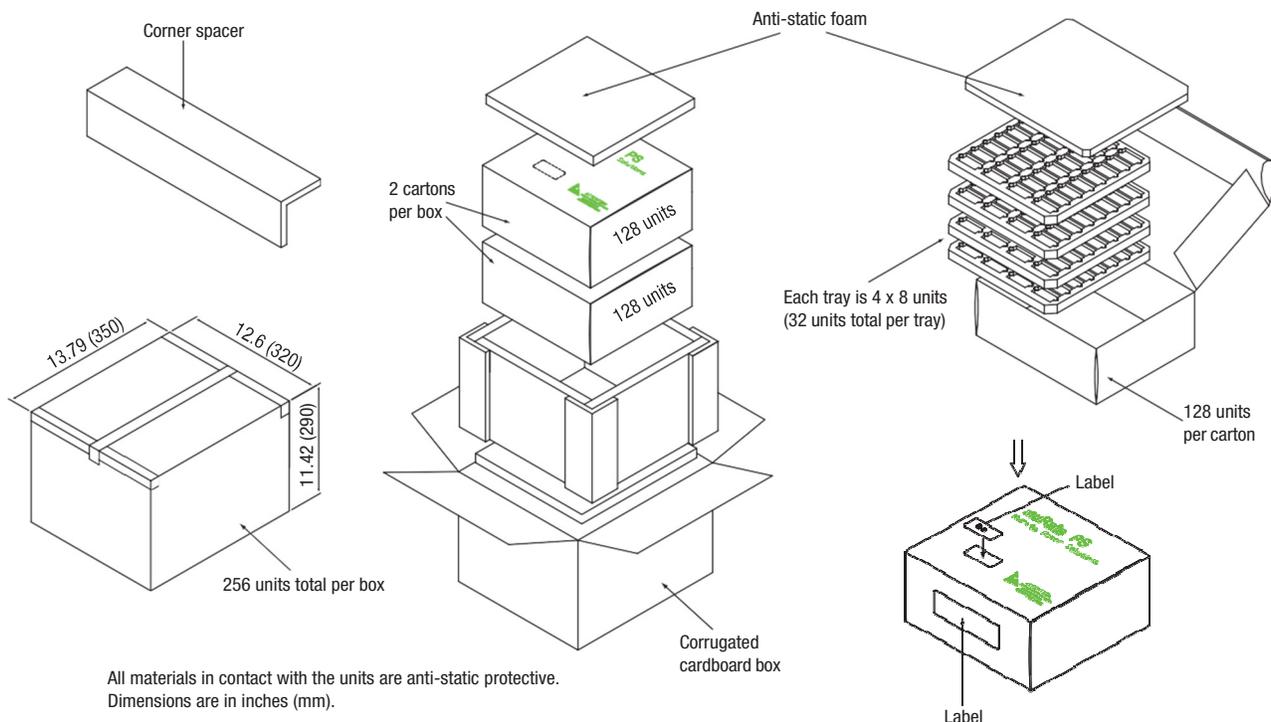
Tolerances (unless otherwise specified):
.XX ± 0.02 (0.5)
.XXX ± 0.010 (0.25)
Angles ± 1°

Components are shown for reference only and may vary between units.

Recommended Footprint (View Through Converter)

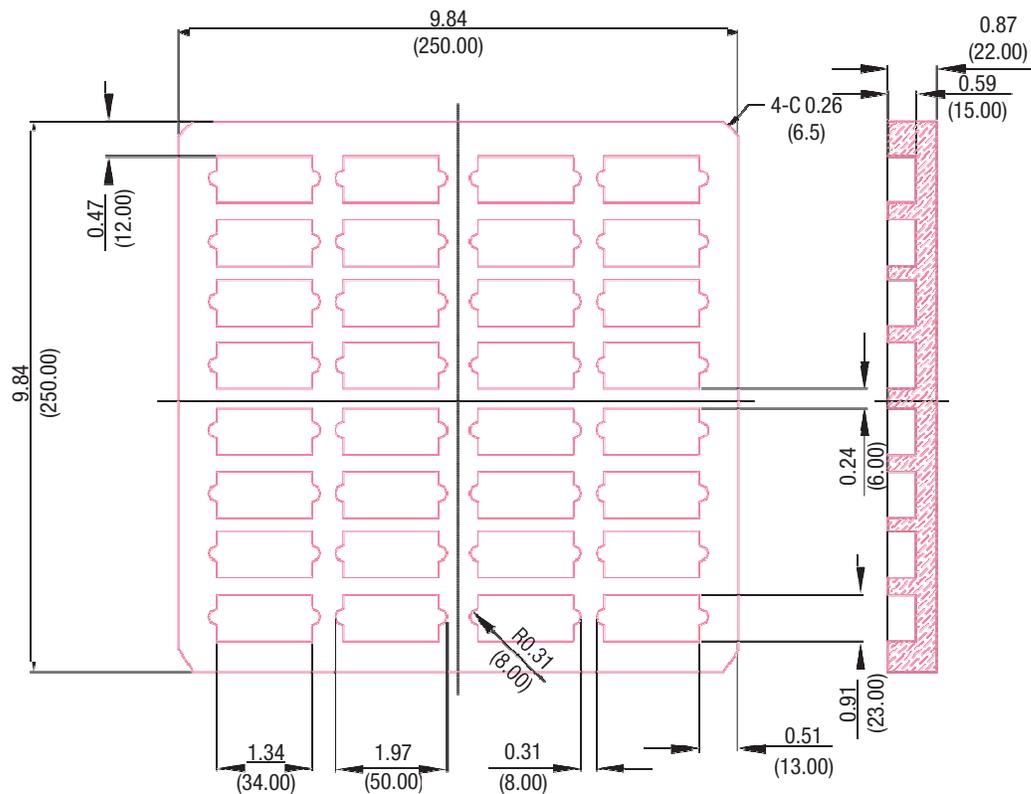


SHIPPING TRAYS AND BOXES, THROUGH-HOLE MOUNT

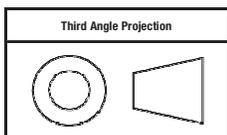


SHIPPING TRAY DIMENSIONS

Material: Low density, closed cell polyethylene anti-static foam

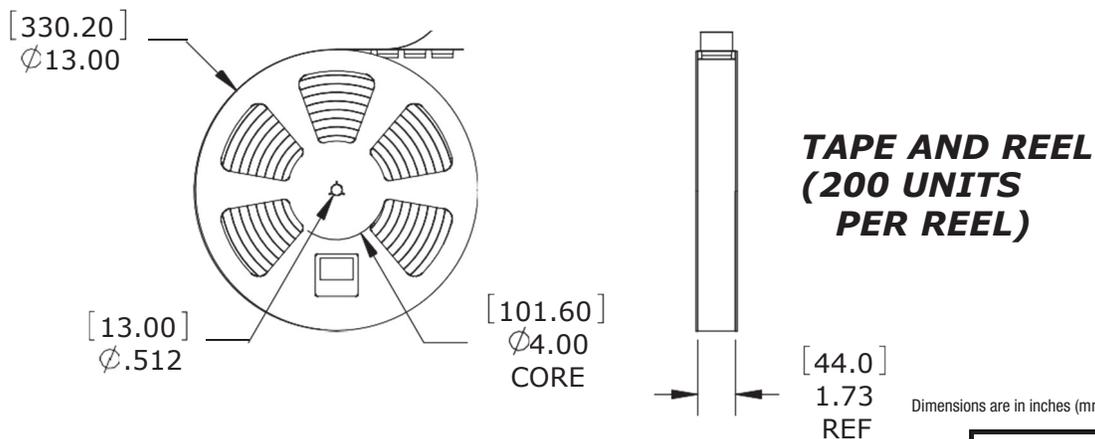
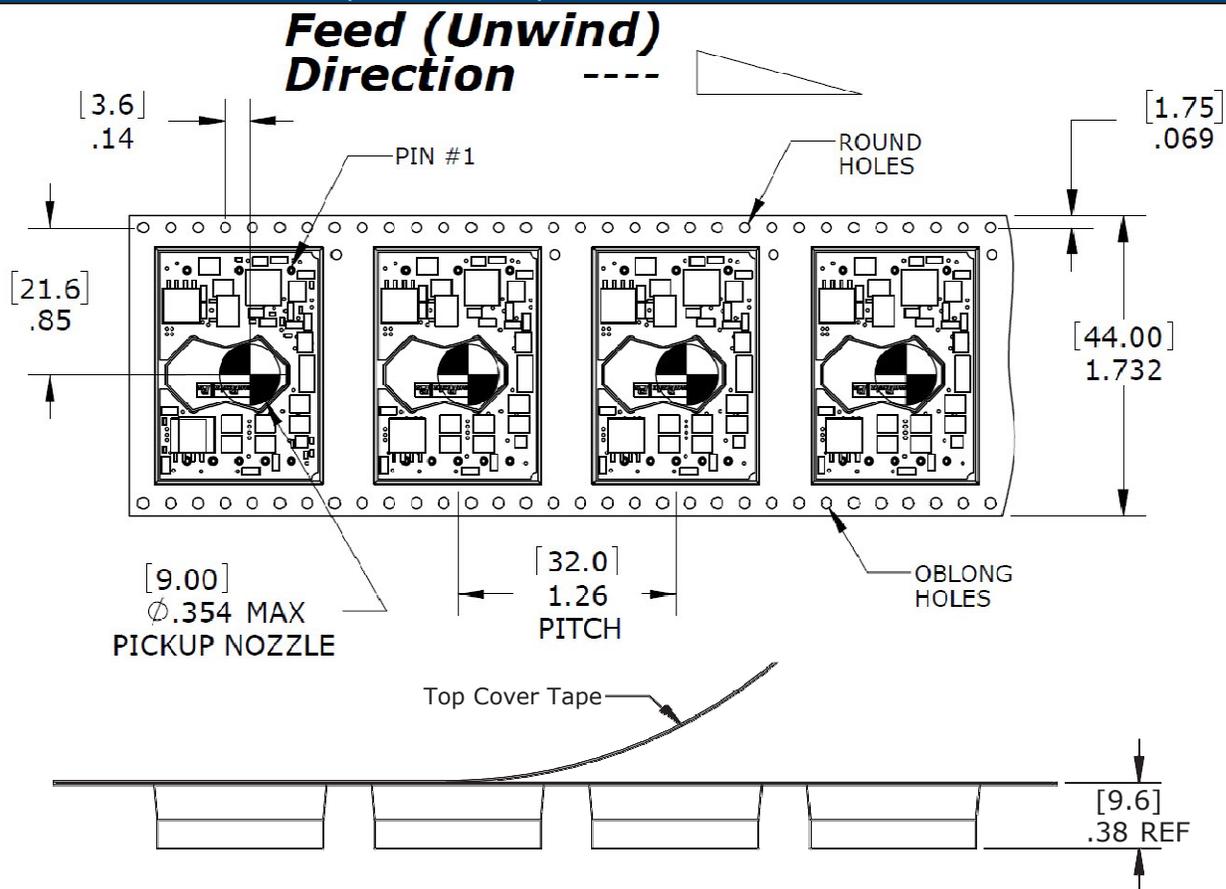


Dimensions are in inches (mm) shown for ref only.

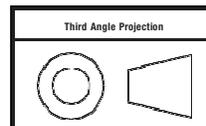


Tolerances (unless otherwise specified):
 .XX ± .02 (0.5)
 .XXX ± .010 (0.25)
 Angles ± 2°

TAPE AND REEL INFORMATION (MSL RATING 2)



Dimensions are in inches (mm shown for ref. only).



Tolerances (unless otherwise specified):
 .XX ± 0.02 (0.5)
 .XXX ± 0.010 (0.25)
 Angles ± 1°

Components are shown for reference only.

TECHNICAL NOTES

Input Fusing

Certain applications and/or safety agencies may require the installation of fuses at the inputs of power conversion components. Fuses should also be used if the possibility of sustained, non-current-limited, input-voltage polarity reversals exists. For Murata Power Solutions UWS series DC-DC converters, we recommend the use of a fast blow fuse, installed in the ungrounded input supply line with a typical value about twice the maximum input current, calculated at low line with the converter's minimum efficiency.

All relevant national and international safety standards and regulations must be observed by the installer. For system safety agency approvals, the converters must be installed in compliance with the requirements of the end-use safety standard, i.e. IEC/UL60950-1.

Input Reverse-Polarity Protection

If the input voltage polarity is accidentally reversed, an internal diode will become forward biased and likely draw excessive current from the power source. If this source is not current limited or the circuit appropriately fused, it could cause permanent damage to the converter.

Input Under-Voltage Shutdown and Start-Up Threshold

Under normal start-up conditions, devices will not begin to regulate properly until the ramping-up input voltage exceeds the Start-Up Threshold Voltage. Once operating, devices will not turn off until the input voltage drops below the Under-Voltage Shutdown limit. Subsequent re-start will not occur until the input is brought back up to the Start-Up Threshold. This built in hysteresis prevents any unstable on/off situations from occurring at a single input voltage.

Start-Up Time

The V_{IN} to V_{OUT} Start-Up Time is the time interval between the point at which the ramping input voltage crosses the Start-Up Threshold and the fully loaded output voltage enters and remains within its specified accuracy band. Actual measured times will vary with input source impedance, external input capacitance, and the slew rate and final value of the input voltage as it appears at the converter. The UWS Series implements a soft start circuit to limit the duty cycle of its PWM controller at power up, thereby limiting the input inrush current.

The On/Off Control to V_{OUT} start-up time assumes the converter has its nominal input voltage applied but is turned off via the On/Off Control pin. The specification defines the interval between the point at which the converter is turned on (released) and the fully loaded output voltage enters and remains within its specified accuracy band. Similar to the V_{IN} to V_{OUT} start-up, the On/Off Control to V_{OUT} start-up time is also governed by the internal soft start circuitry and external load capacitance. The difference in start up time from V_{IN} to V_{OUT} and from On/Off Control to V_{OUT} is therefore insignificant.

Input Source Impedance

The input of UWS converters must be driven from a low ac-impedance source. The DC-DC's performance and stability can be compromised by the use of highly inductive source impedances. The input circuit shown in Figure 2 is a practical solution that can be used to minimize the effects of inductance in the input traces. For optimum performance, components should be mounted close to the DC-DC converter.

I/O Filtering, Input Ripple Current, and Output Noise

All models in the UWS Series are tested/specified for input reflected ripple current and output noise using the specified external input/output components/circuits and layout as shown in the following two figures. External input capacitors (C_{IN} in Figure 2) serve primarily as energy-storage elements, minimizing line voltage variations caused by transient IR drops in conductors from backplane to the DC-DC. Input caps should be selected for bulk capacitance (at appropriate frequencies), low ESR, and high rms-ripple-current ratings. The switching nature of DC-DC converters requires that dc voltage sources have low ac impedance as highly inductive source impedance can affect system stability. In Figure 2, C_{BUS} and L_{BUS} simulate a typical dc voltage bus. Your specific system configuration may necessitate additional considerations.

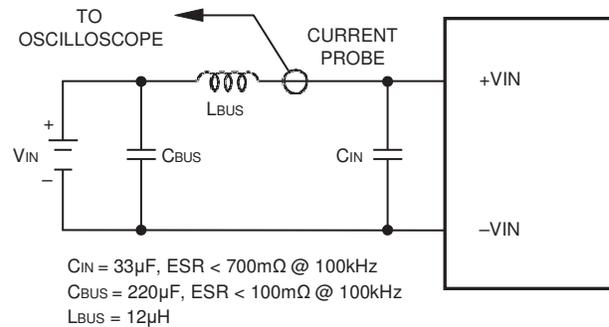


Figure 2. Measuring Input Ripple Current

In critical applications, output ripple/noise (also referred to as periodic and random deviations or PARD) may be reduced below specified limits using filtering techniques, the simplest of which is the installation of additional external output capacitors. They function as true filter elements and should be selected for bulk capacitance, low ESR and appropriate frequency response.

All external capacitors should have appropriate voltage ratings and be located as close to the converter as possible. Temperature variations for all relevant parameters should also be taken carefully into consideration. The most effective combination of external I/O capacitors will be a function of line voltage and source impedance, as well as particular load and layout conditions.

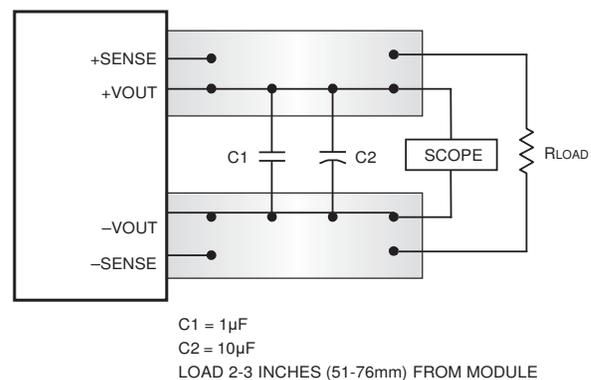


Figure 3. Measuring Output Ripple/Noise (PARD)

Floating Outputs

Since these are isolated DC-DC converters, their outputs are “floating” with respect to their input. Designers will normally use the –Output as the ground/return of the load circuit. You can however, use the +Output as ground/return to effectively reverse the output polarity.

Minimum Output Loading Requirements

UWS converters employ a synchronous-rectifier design topology and all models regulate within spec and are stable under no-load to full load conditions. Operation under no-load conditions however might slightly increase the output ripple and noise.

Thermal Shutdown

The UWS converters are equipped with thermal-shutdown circuitry. If environmental conditions cause the temperature of the DC-DC converter to rise above the designed operating temperature, a precision temperature sensor will power down the unit. When the internal temperature decreases below the threshold of the temperature sensor, the unit will self start. See Performance/Functional Specifications.

Output Over-Voltage Protection

The UWS output voltage is monitored for an over-voltage condition using a comparator. The signal is optically coupled to the primary side and if the output voltage rises to a level which could be damaging to the load, the sensing circuitry will power down the PWM controller causing the output voltage to decrease. Following a time-out period the PWM will restart, causing the output voltage to ramp to its appropriate value. If the fault condition persists, and the output voltage again climbs to excessive levels, the over-voltage circuitry will initiate another shutdown cycle. This on/off cycling is referred to as “hiccup” mode.

Current Limiting

As soon as the output current increases to approximately 130% of its rated value, the DC-DC converter will go into a current-limiting mode. In this condition, the output voltage will decrease proportionately with increases in output current, thereby maintaining somewhat constant power dissipation. This is commonly referred to as power limiting. Current limit inception is defined as the point at which the full-power output voltage falls below the specified tolerance. See Performance/Functional Specifications. If the load current, being drawn from the converter, is significant enough, the unit will go into a short circuit condition as described below.

Short Circuit Condition

When a converter is in current-limit mode, the output voltage will drop as the output current demand increases. If the output voltage drops too low, the magnetically coupled voltage used to develop primary side voltages will also drop, thereby shutting down the PWM controller. Following a time-out period, the PWM will restart causing the output voltage to begin ramping to their appropriate value. If the short-circuit condition persists, another shutdown cycle will be initiated. This on/off cycling is referred to as “hiccup” mode. The hiccup cycling reduces the average output current, thereby preventing internal temperatures from rising to excessive levels. The UWS Series is capable of enduring an indefinite short circuit output condition.

Remote Sense (models UWS-3.3/15-Q48 and UWS-5/10-Q48 only)

Note: The Sense and V_{OUT} lines are internally connected through low-value resistors. Nevertheless, if the sense function is not used for remote regulation the user should connect the +Sense to +V_{OUT} and –Sense to –V_{OUT} at the DC-DC converter pins. ULS series converters employ a sense feature to provide point of use regulation, thereby overcoming moderate IR drops in PCB conductors or cabling. The remote sense lines carry very little current and therefore require minimal cross-sectional-area conductors. The sense lines, which are capacitively coupled to their respective output lines, are used by the feedback control-loop to regulate the output. As such, they are not low impedance points and must be treated with care in layouts and cabling. Sense lines on a PCB should be run adjacent to dc signals, preferably ground.

$$[V_{OUT(+)} - V_{OUT(-)}] - [Sense(+)-Sense(-)] \leq 10\%V_{OUT}$$

In cables and discrete wiring applications, twisted pair or other techniques should be used. Output over-voltage protection is monitored at the output voltage pin, not the Sense pin. Therefore, excessive voltage differences between V_{OUT} and Sense in conjunction with trim adjustment of the output voltage can cause the over-voltage protection circuitry to activate (see Performance Specifications for over-voltage limits). Power derating is based on maximum output current and voltage at the converter’s output pins. Use of trim and sense functions can cause output voltages to increase, thereby increasing output power beyond the converter’s specified rating, or cause output voltages to climb into the output over-voltage region. Therefore, the designer must ensure:

$$(V_{OUT \text{ at pins}}) \times (I_{OUT}) \leq \text{rated output power}$$

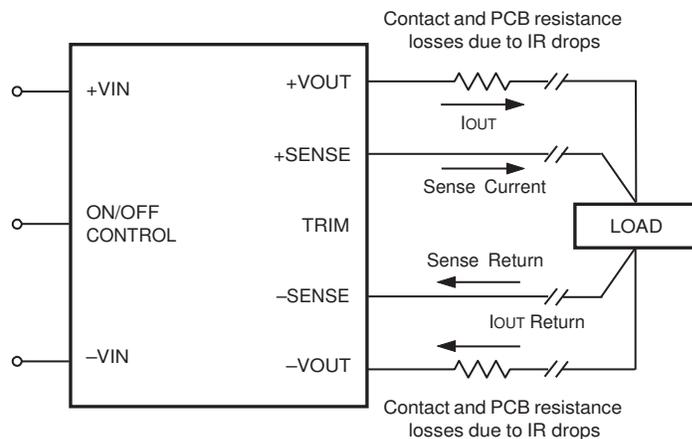


Figure 4. Remote Sense Circuit Configuration

Sense is included only on models UWS-3.3/15-Q48 and UWS-5/10-Q48.

On/Off Control

The input-side, remote On/Off Control function can be ordered to operate with either logic type:

Positive ("P" suffix) logic models are enabled when the On/Off pin is left open or is pulled high (see specifications) with respect to the -Input as per Figure 4. Positive-logic devices are disabled when the on/off pin is pulled low with respect to the -Input.

Negative ("N" suffix) logic devices are off when the On/Off pin is left open or is pulled high (see specifications), and on when the pin is pulled low with respect to the -Input. See specifications.

Dynamic control of the remote on/off function is best accomplished with a mechanical relay or an open-collector/open-drain drive circuit (optically isolated if appropriate). The drive circuit should be able to sink appropriate current (see Performance Specifications) when activated and withstand appropriate voltage when deactivated. Applying an external voltage to pin 2 when no input power is applied to the converter can cause permanent damage to the converter.

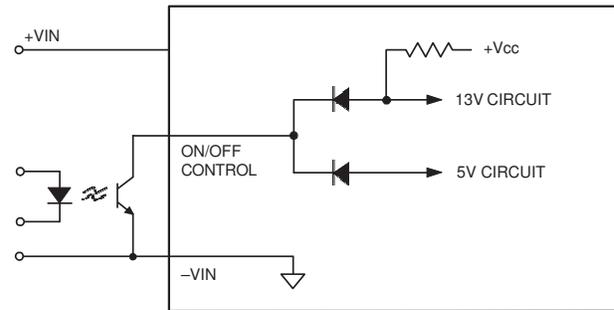


Figure 5. Driving the Negative Logic On/Off Control Pin (simplified circuit)

Trim Equations

Trim Down
$R_{T_DOWN} (k\Omega) = \frac{511}{\Delta\%} - 10.22$
Where $\Delta\% = \left(\frac{V_{NOM} - V_{DES}}{V_{NOM}} \times 100 \right)$
Trim Up
$R_{T_UP} (k\Omega) = \frac{5.11 \times V_{NOM} \times (100 + \Delta\%)}{1.225 \times \Delta\%} - \frac{511}{\Delta\%} - 10.22$
<p>Note: "Δ%" is always a positive value. "V_{NOM}" is the nominal, rated output voltage. "V_{DES}" is the desired, changed output voltage.</p>

OUTPUT VOLTAGE ADJUSTMENT

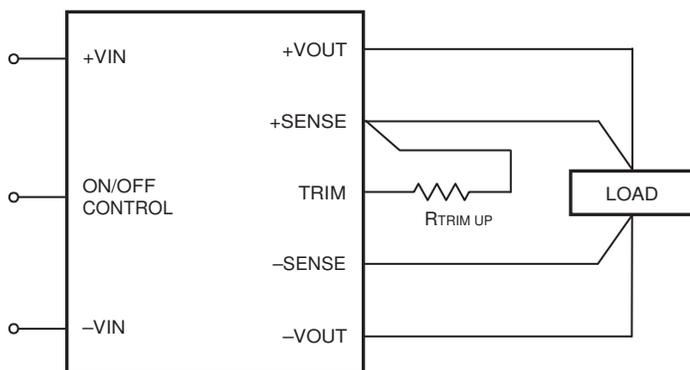


Figure 6. Trim Connections to Increase Output Voltages

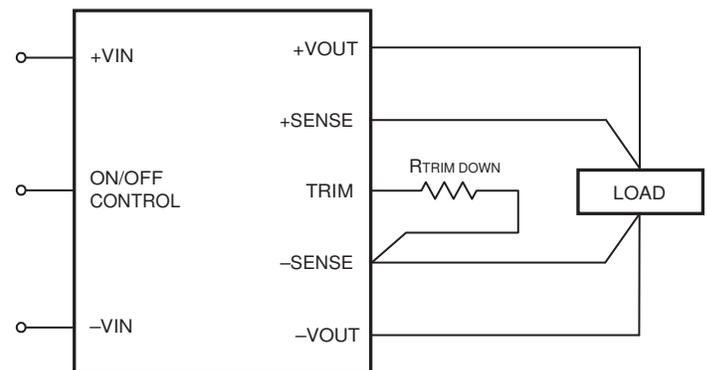


Figure 7. Trim Connections to Decrease Output Voltages

Sense is included on UWS-3.3/15-D48 and UWS-5/10-Q48. Connect Trim to the respective Vout pin if sense is not installed.

Sixteenth-brick DOSA-Compatible,
Wide Input Isolated DC-DC Converters

Vertical Wind Tunnel

Murata Power Solutions employs a computer controlled custom-designed closed loop vertical wind tunnel, infrared video camera system, and test instrumentation for accurate airflow and heat dissipation analysis of power products. The system includes a precision low flow-rate anemometer, variable speed fan, power supply input and load controls, temperature gauges, and adjustable heating element.

The IR camera monitors the thermal performance of the Unit Under Test (UUT) under static steady-state conditions. A special optical port is used which is transparent to infrared wavelengths.

Both through-hole and surface mount converters are soldered down to a 10" x 10" host carrier board for realistic heat absorption and spreading. Both longitudinal and transverse airflow studies are possible by rotation of this carrier board since there are often significant differences in the heat dissipation in the two airflow directions. The combination of adjustable airflow, adjustable ambient heat, and adjustable Input/Output currents and voltages mean that a very wide range of measurement conditions can be studied.

The collimator reduces the amount of turbulence adjacent to the UUT by minimizing airflow turbulence. Such turbulence influences the effective heat transfer characteristics and gives false readings. Excess turbulence removes more heat from some surfaces and less heat from others, possibly causing uneven overheating.

Both sides of the UUT are studied since there are different thermal gradients on each side. The adjustable heating element and fan, built-in temperature gauges, and no-contact IR camera mean that power supplies are tested in real-world conditions.

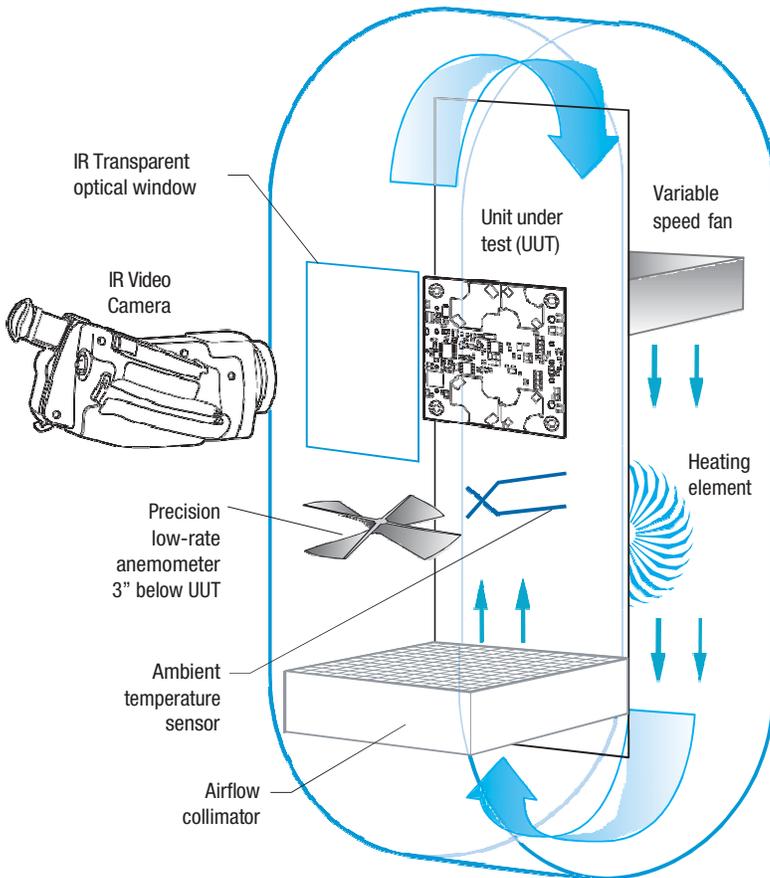


Figure 8. Vertical Wind Tunnel

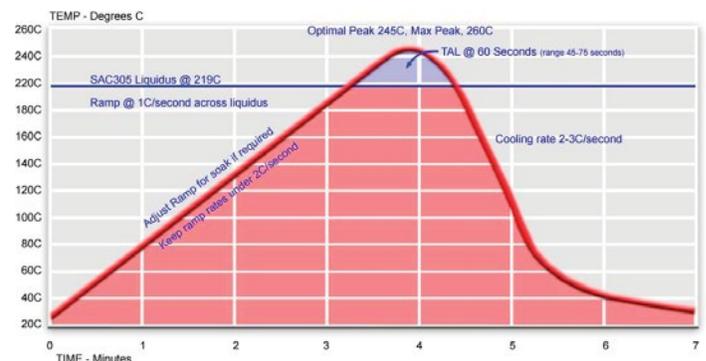
Through-Hole Soldering Guidelines

Murata Power Solutions recommends the TH soldering specifications below when installing these converters. These specifications vary depending on the solder type. Exceeding these specifications may cause damage to the product. Your production environment may differ; therefore, please thoroughly review these guidelines with your Process Engineers.

Wave Solder Operations for Through-Hole mounted products (THMT)	
For Sn/Ag/Cu based solders:	
Maximum Preheat Temperature	115° C.
Maximum Pot Temperature	270° C.
Maximum Solder Dwell Time	7 seconds
For Sn/Pb based solders:	
Maximum Preheat Temperature	105° C.
Maximum Pot Temperature	250° C.
Maximum Solder Dwell Time	6 seconds

SMT Reflow Soldering Guidelines

The surface-mount reflow solder profile shown below is suitable for SAC305 type lead-free solders. This graph should be used only as a **guideline**. Many other factors influence the success of SMT reflow soldering. Since your production environment may differ, please thoroughly review these guidelines with your process engineers.



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ISO 9001 and 14001 REGISTERED



**This product is subject to the following operating requirements and the Life and Safety Critical Application Sales Policy:
Refer to: <http://www.murata-ps.com/requirements/>**

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